

Berry-Constructive Programs are Sequentially Constructive

Or:

Synchronous Programming from a Scheduling Perspective

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Aims of Work

- **Proof of Claim** [von Hanxleden, Mendler, Aguado, et. al. DATE 2013] that **Sequential Constructiveness** is a conservative extension of **Berry-Constructiveness**.
- **New domain-theoretic presentation** of Berry's constructive „must-cannot“ analysis for pure Esterel:
 - Reconstruction of imperative synchronous programming for **multi-threaded shared-variable programs** (from a scheduling perspective)
 - New **sequential-concurrent reaction model**
 - Captures **explicit signal initialisation**

Structure of Talk

1. Introduction
2. Sequential Constructiveness (Scheduling)
3. Berry Constructiveness (Domain)
4. Examples
5. Conclusion

1 INTRODUCTION

Widening the Scope of a Logical Clock

- Lustre/Signal oversampling clock abstraction
- Quartz clock refinement [Gemünde,Brandt,Schneider 2009]
- Reactive ML clock domains [Mandel,Pasteur,Pouzet 2013]
- Bursty integer clocks [Guatto, et.al. 2012]
- ...



logical clock

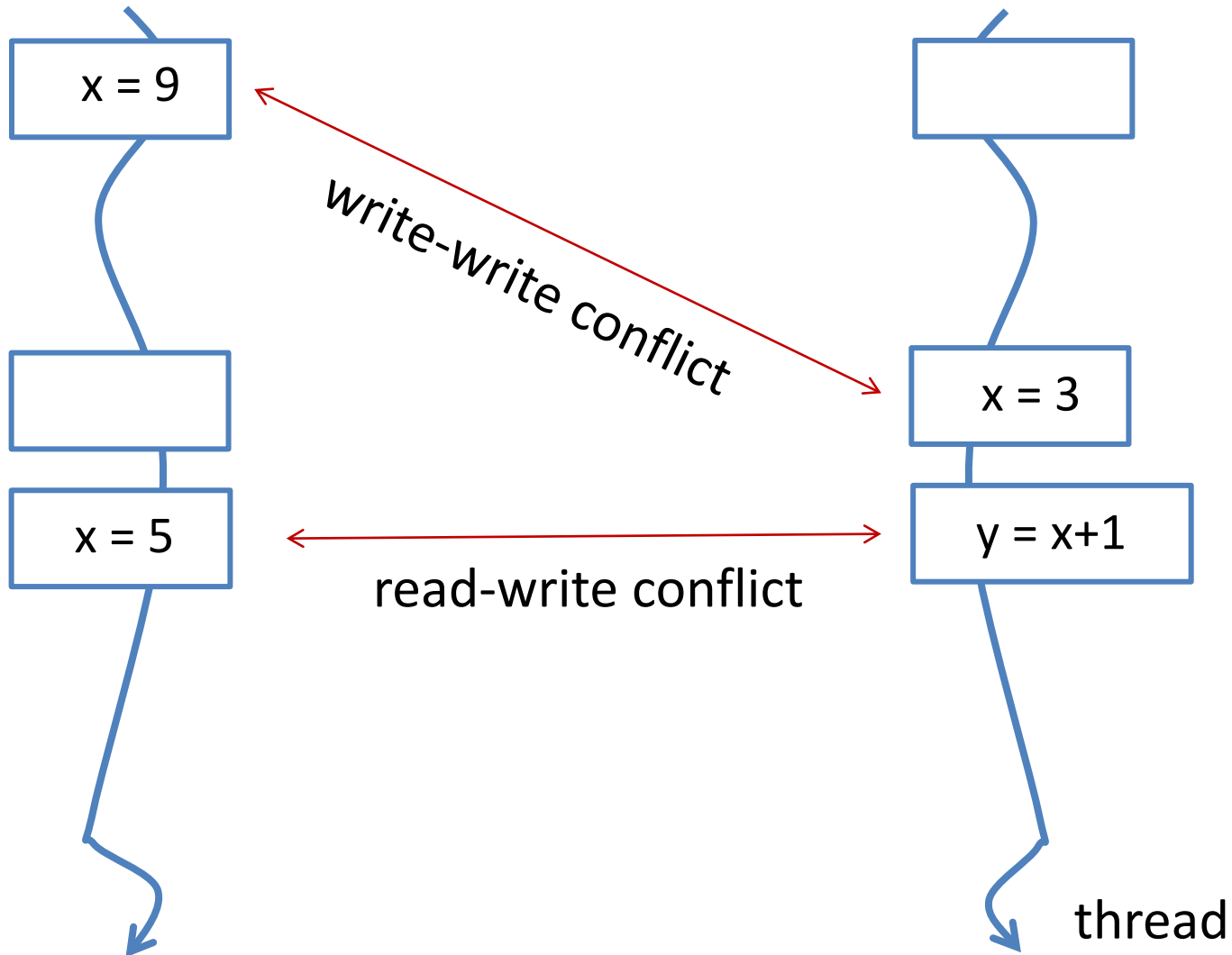
on-a logical clock

on-a logical clock

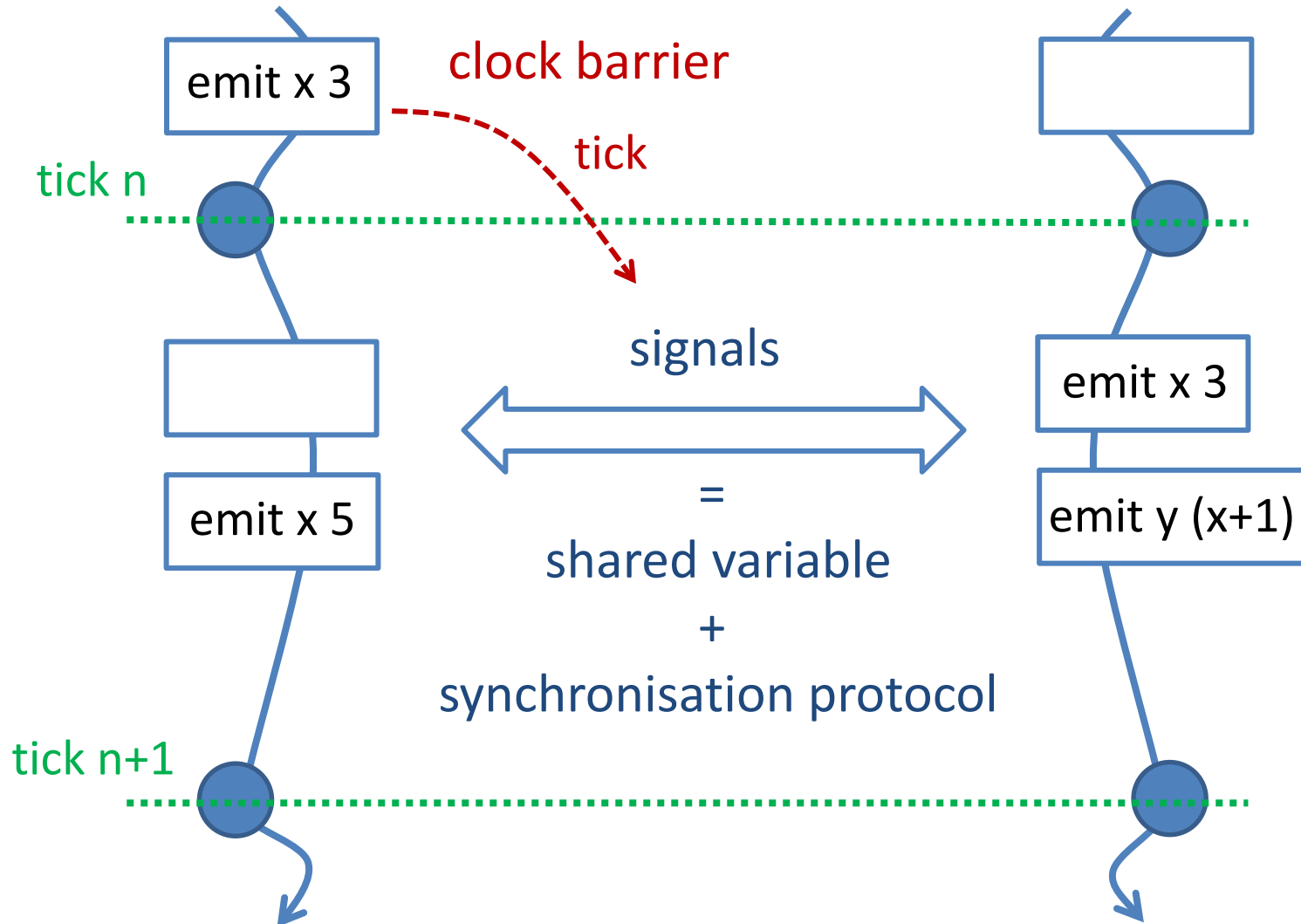
on-a sequential composition

nesting can lead to over-synchronisation...

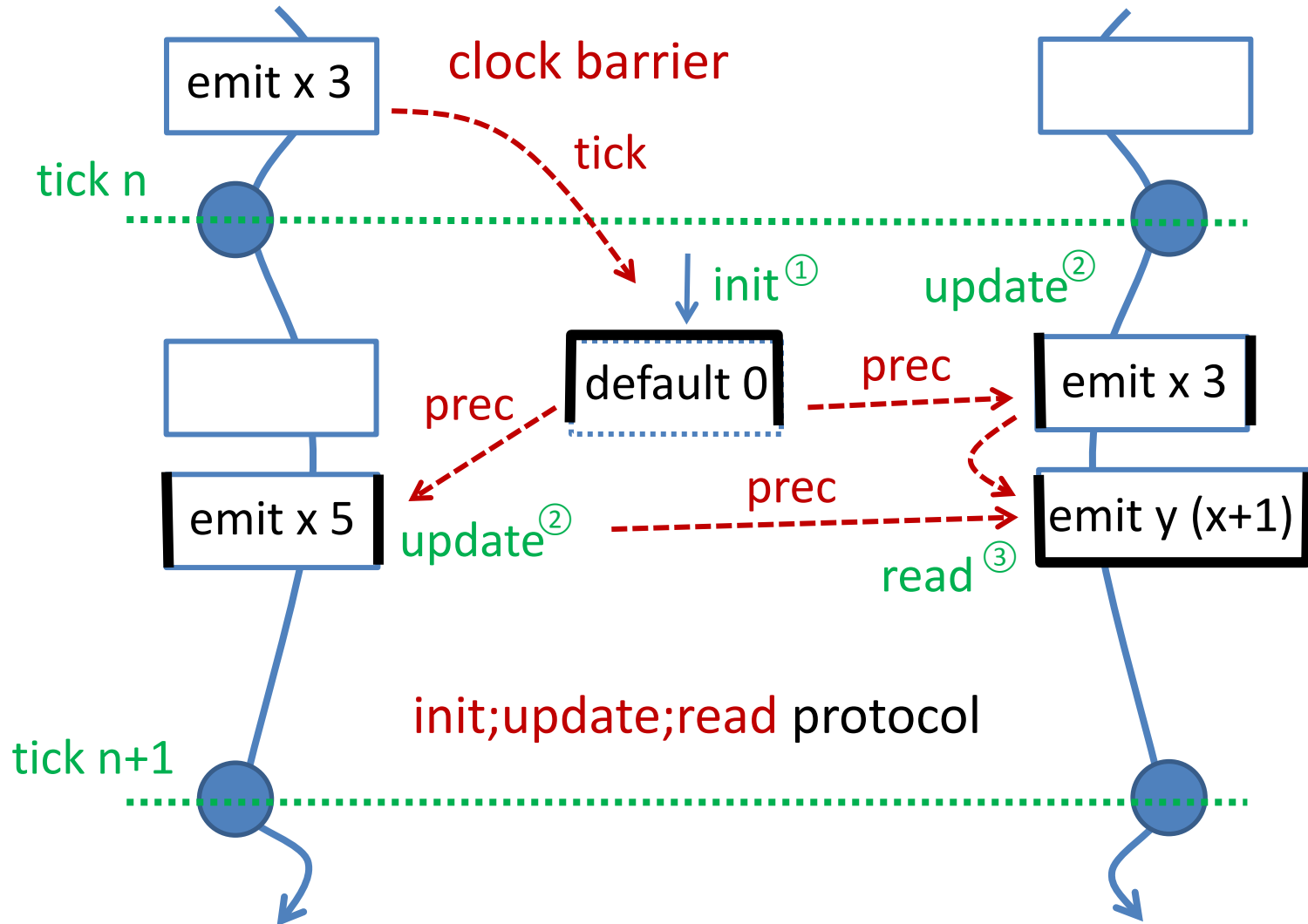
Shared Memory Multithreading



Clocked Shared Signal Multithreading

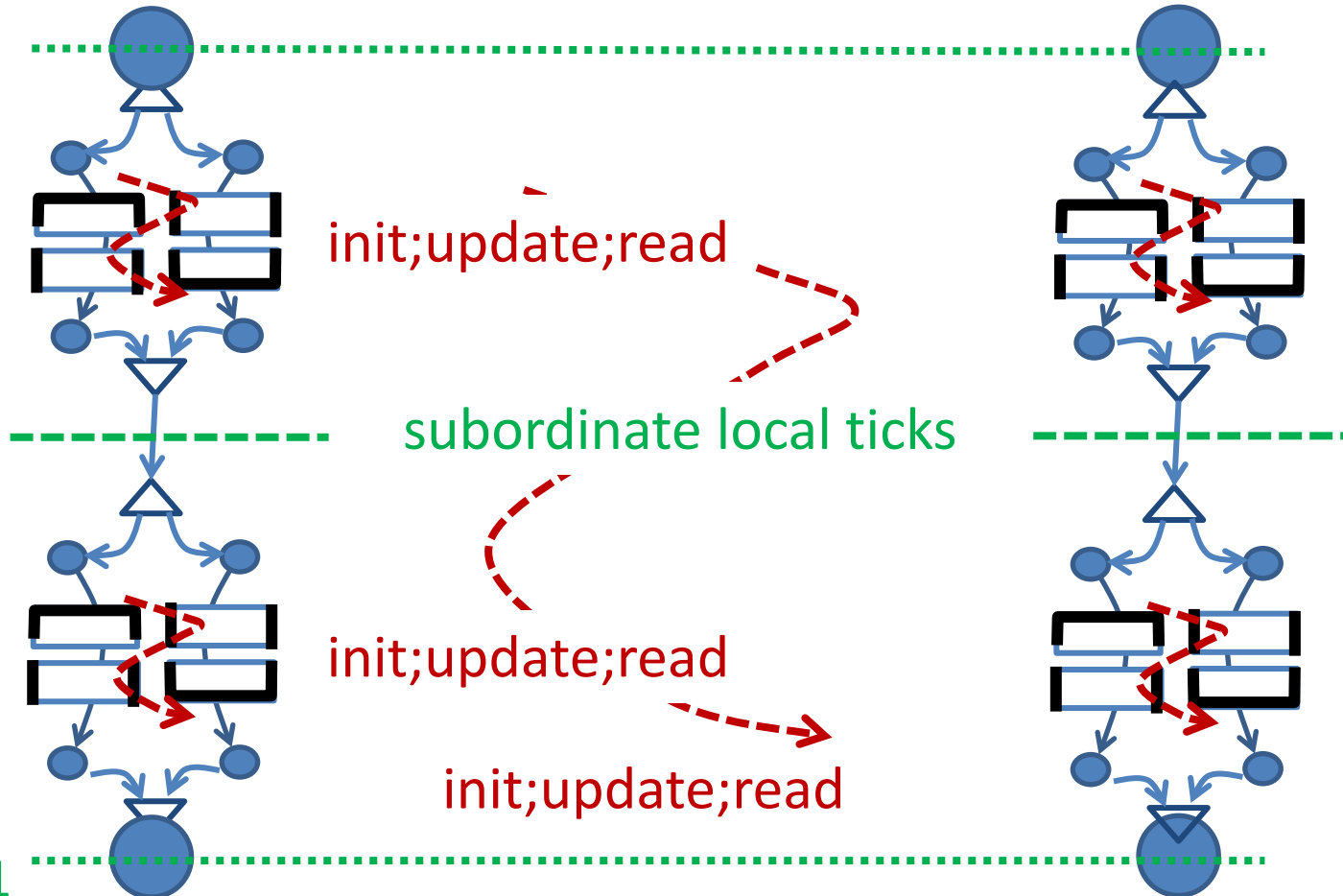


Clocked Shared Signal Multithreading



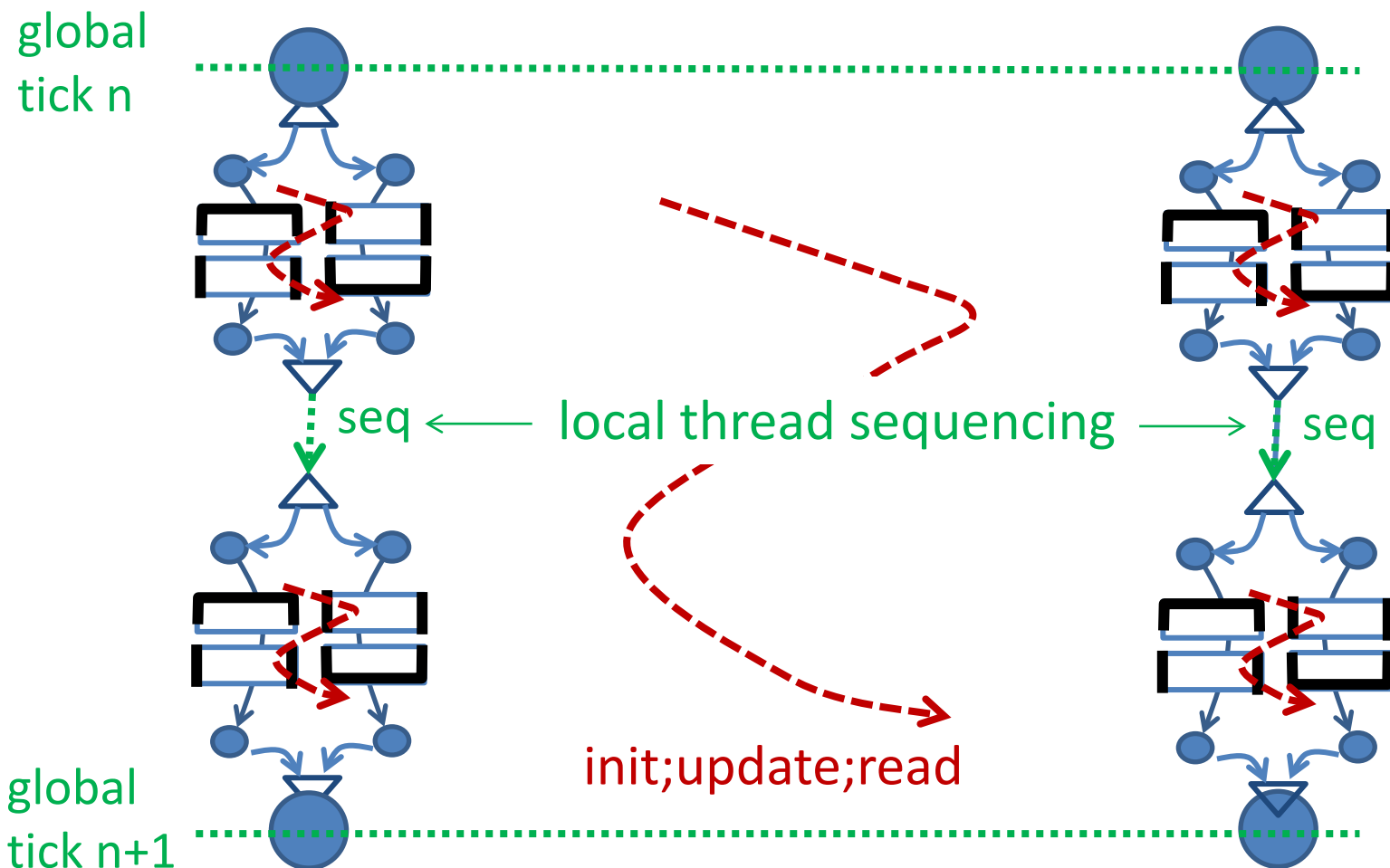
Clock Refinement

global
tick n



global
tick n+1

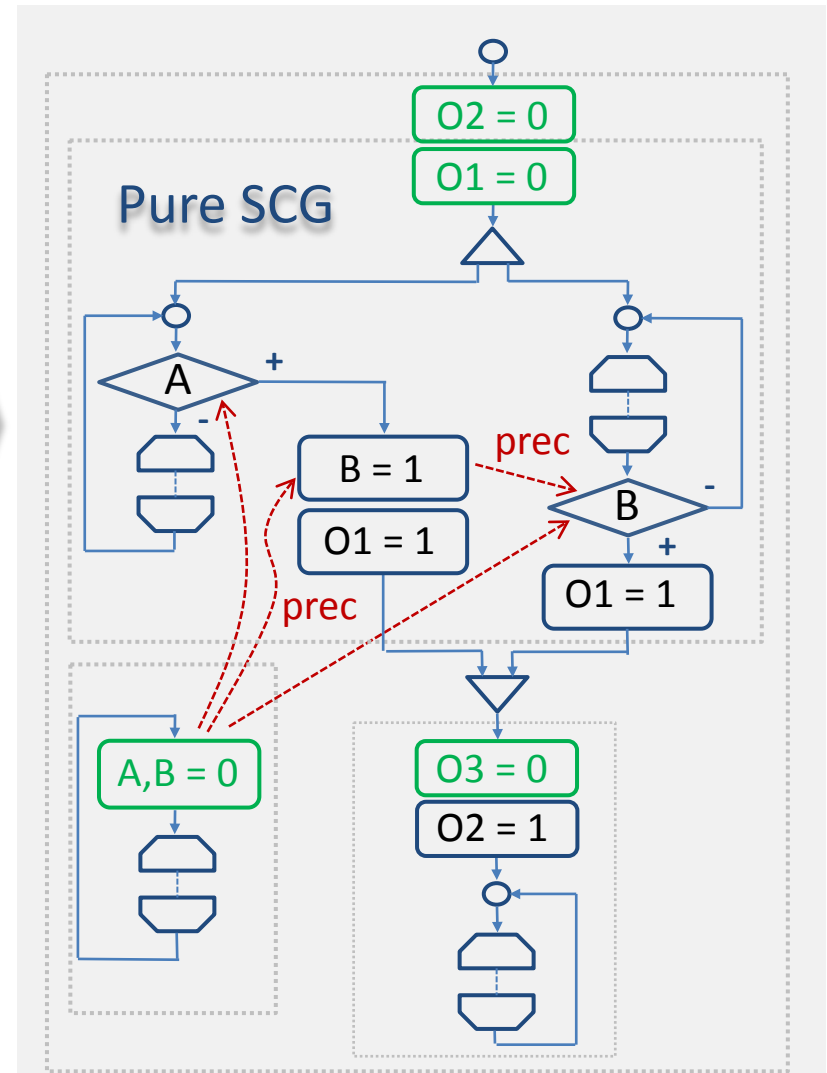
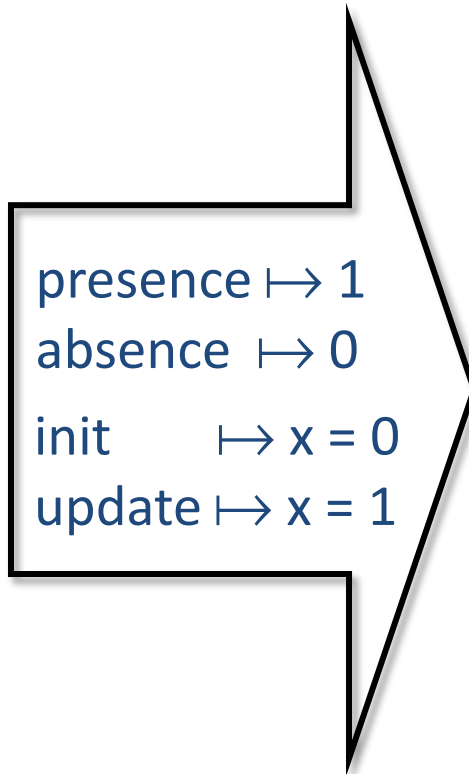
Prescriptive Sequentiality Replaces Local Clock



2 SEQUENTIAL CONSTRUCTIVENESS

Pure Signals = Synchronised Boolean Variables

```
module ABOs— Esterel
inputoutput A,B;
signal O2 in
signal O1 in
[ % Thread A
  await immediate A;
  emit B;
  emit O1;
||
% Thread B
  await B;
  emit O1;
];
end;
signal O3 in
  emit O2;
end;
halt;
end
```



Synchronous Instants

A **synchronous instant** is a maximal micro-sequence

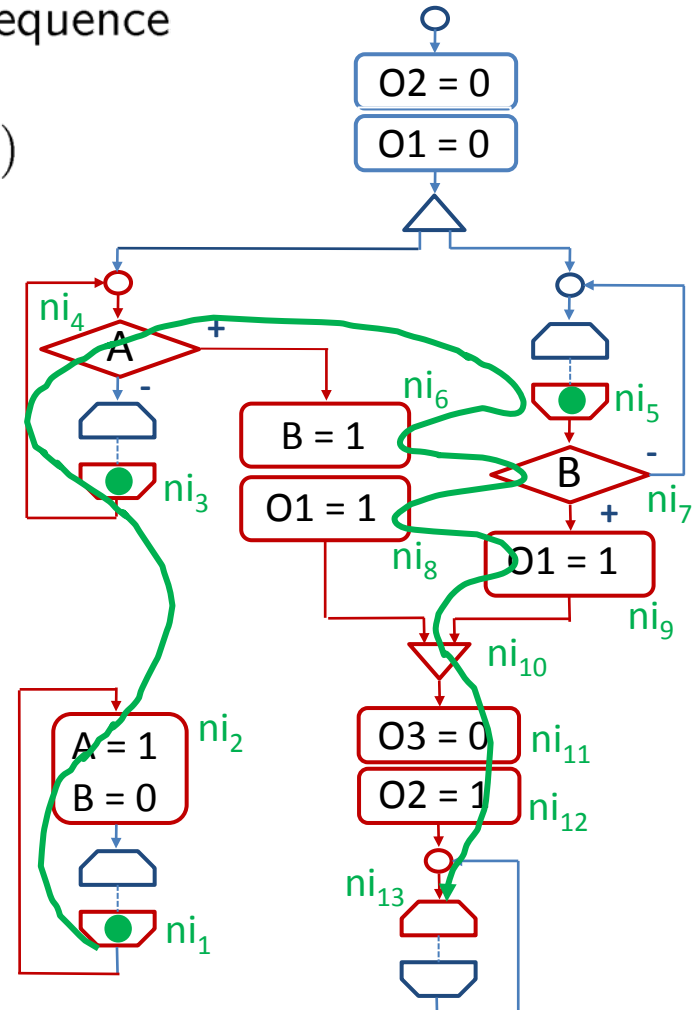
$$R : (\Sigma_0, \rho_0) \xrightarrow{\mu s}^{ni_1} (\Sigma_1, \rho_1) \cdots \xrightarrow{\mu s}^{ni_k} (\Sigma_k, \rho_k)$$

of node instances $ni_j = R(j)$.

The **synchronisation protocol** depends on ...

- **concurrency** $ni_6 \mid_R ni_9$
- **precedence** $ni_6 \rightarrow_{prec} ni_7$
- **confluence** $ni_8 \sim_R ni_9$

of node instances.



Concurrency, Precedence, Confluence (Informal)

- $ni_1 \mid_R ni_2$: ni_1 and ni_2 are **concurrent** in R if
 - ni_1 and ni_2 have been instantiated in R as siblings of the same common (least ancestor) fork node.
- $ni_1 \rightarrow_{prec} ni_2$: ni_1 **precedes** ni_2 in the **init;update;read** protocol if
 - $ni_1 \mid_R ni_2$,
 - ni_1 performs an **init/update** on a variable that is **read** by ni_2 , or
 - ni_1 performs an **init** and ni_2 performs an **update**.
- $ni_1 \sim_R ni_2$: ni_1 and ni_2 are **confluent** in R if
 - ni_1 and ni_2 can be executed in any order, with the same resulting configuration.

Sequential Constructiveness SC

- A micro-sequence R is **sequentially admissible** iff for all node instances the following **scheduling condition** is satisfied:

If $ni_{j_1} \rightarrow_{prec} ni_{j_2}$, then $j_1 \leq j_2$ or $ni_{j_1} \sim_R ni_{j_2}$.

- A (**closed**) process P is **sequentially constructive** (SC) iff for all admissibly reachable configurations (Σ_0, ρ_0) of P :
 - **there exists** a sequentially admissible synchronous instant $R : (\Sigma_0, \rho_0) \xrightarrow{ni_1}_{\mu s} (\Sigma_1, \rho_1) \cdots \xrightarrow{ni_k}_{\mu s} (\Sigma_k, \rho_k)$ and
 - **every** sequentially admissible synchronous instant R leads to the same quiescent response ρ_k .

Conservative Approximations of SC

Static Approximation

- A program P is **ASC-schedulable** iff no control-flow cycle in the SCG of P contains a \rightarrow_{prec} dependency edge.

Theorem 1 (DATE 2013). *Every ASC-schedulable program is SC.*

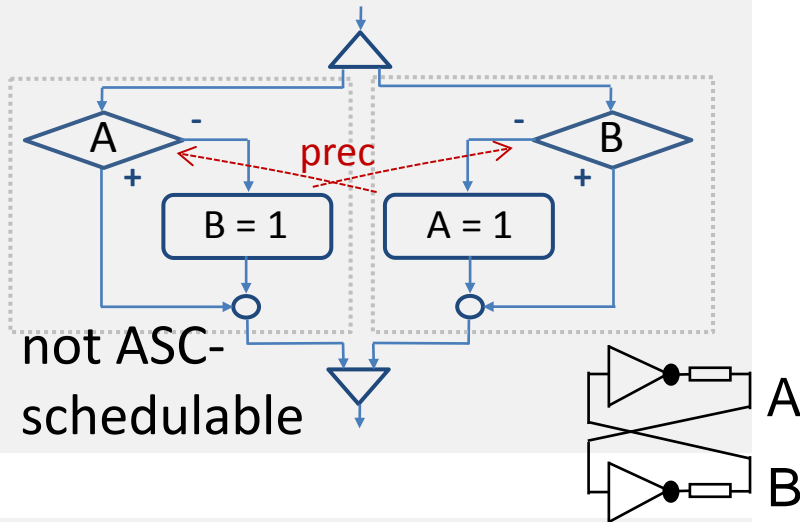
Data-dependent Approximation

Theorem 2 (New). *Every Berry-constructive program is SC.*

What means Berry-constructive in the shared-variable setting with explicit initialisations ? ...

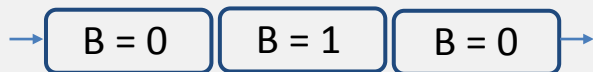
Examples

not sequentially constructive

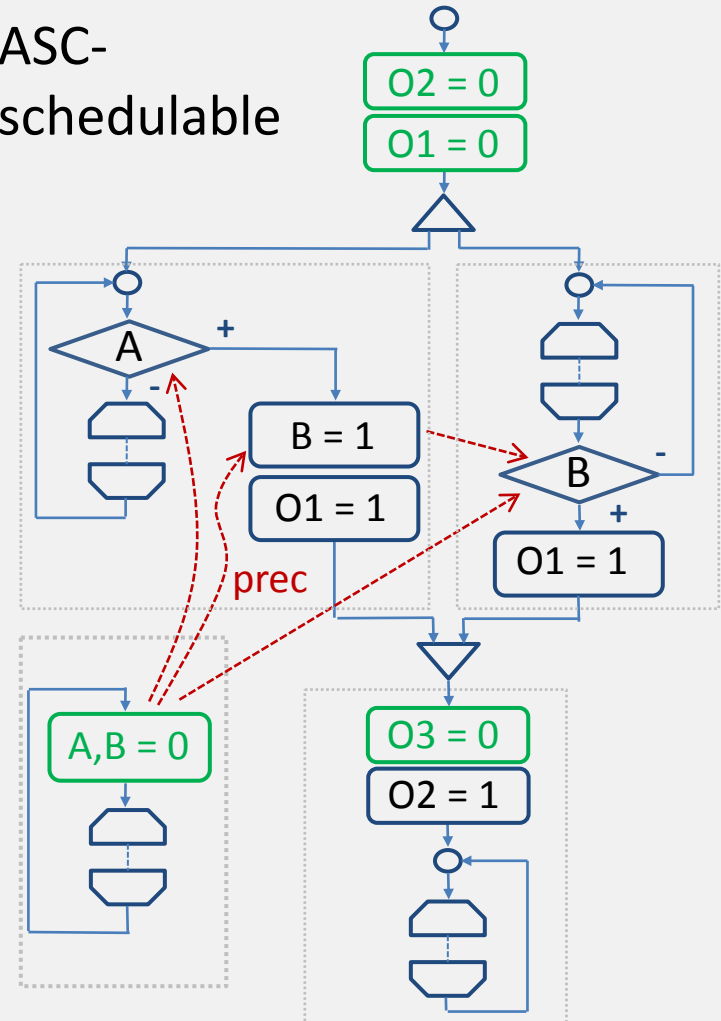


not ASC-schedulable

Every non-concurrent program is sequentially constructive



ASC-schedulable



3 BERRY CONSTRUCTIVENESS

BC Semantics – Variable Status

- Variables \mathcal{V} participate in the **synchronisation protocol** by changing **status** in a **linear** 4-valued domain

$$\begin{aligned}(\mathbb{D}, \leq) &= \{\perp \leq 0 \leq 1 \leq \top\} \\ &= \{\text{pristine} \leq \text{initialised} \leq \text{updated} \leq \text{crashed}\}\end{aligned}$$

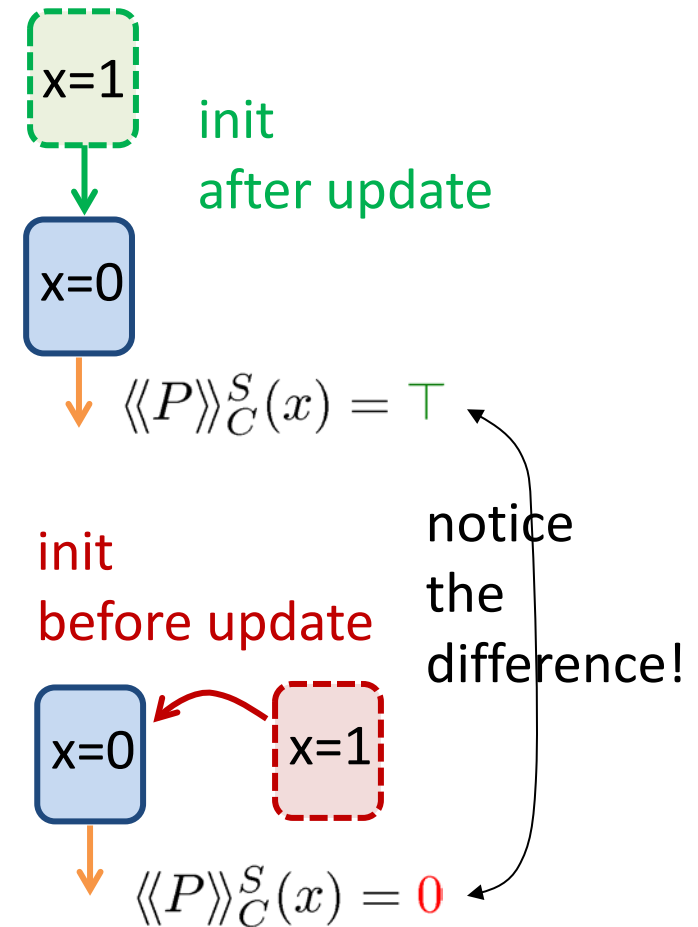
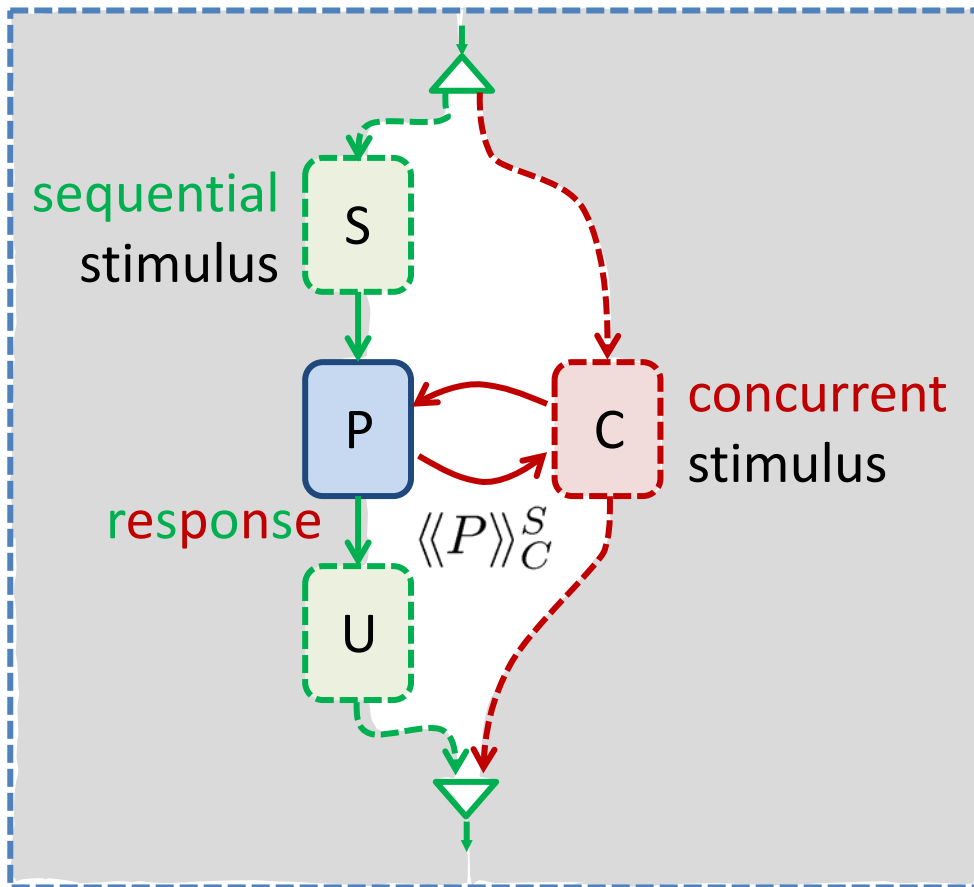
A variable $s \in \mathcal{V}$ with status $\gamma \in \mathbb{D}$ is denoted by s^γ .

In the **fixed point analysis** these values are approximated using

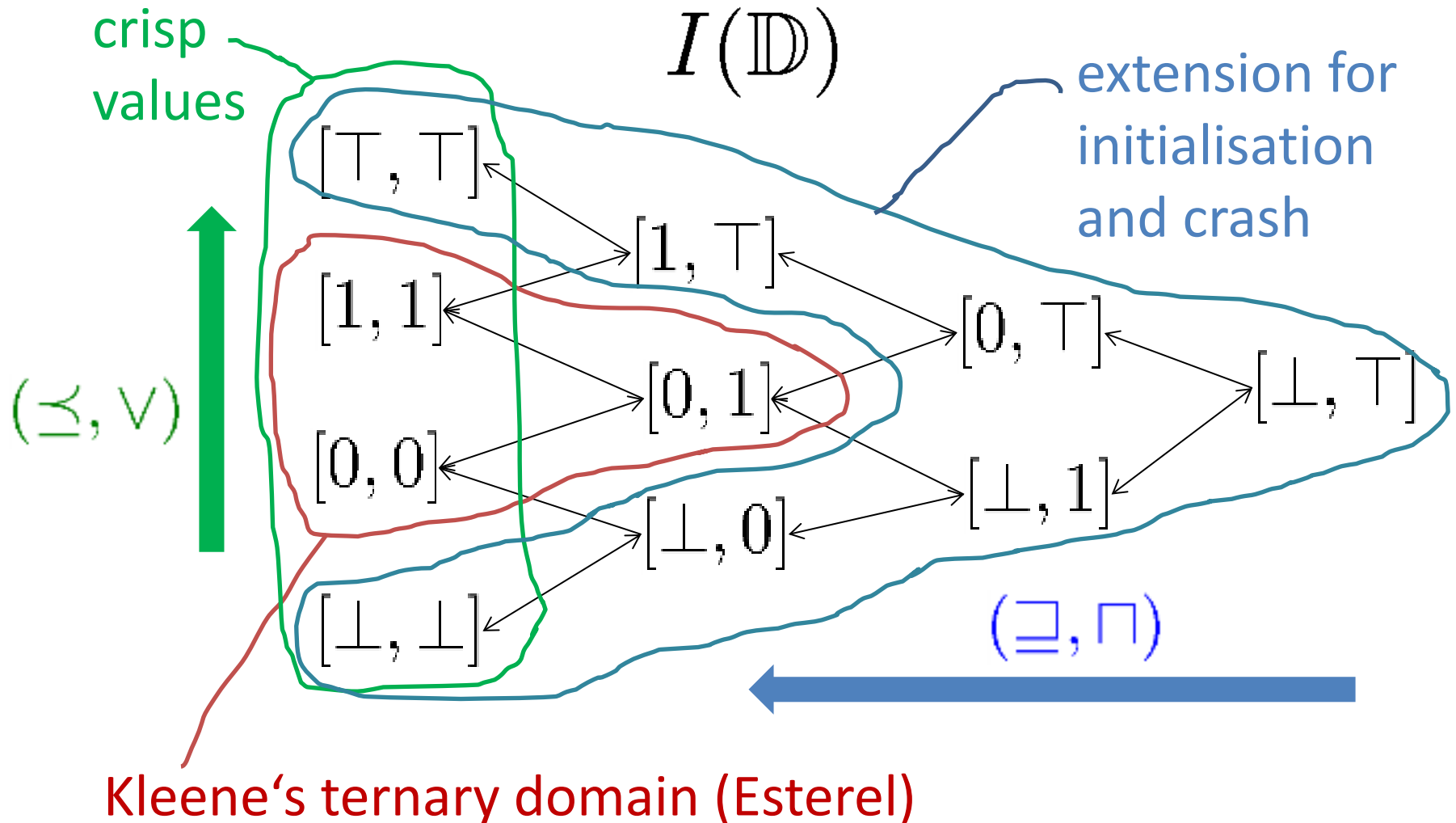
- **intervals** ...
- in a 2-dimensional **sequential-concurrent reaction model** ...

Sequential-Concurrent (SC) Reaction Model

Program P in Context

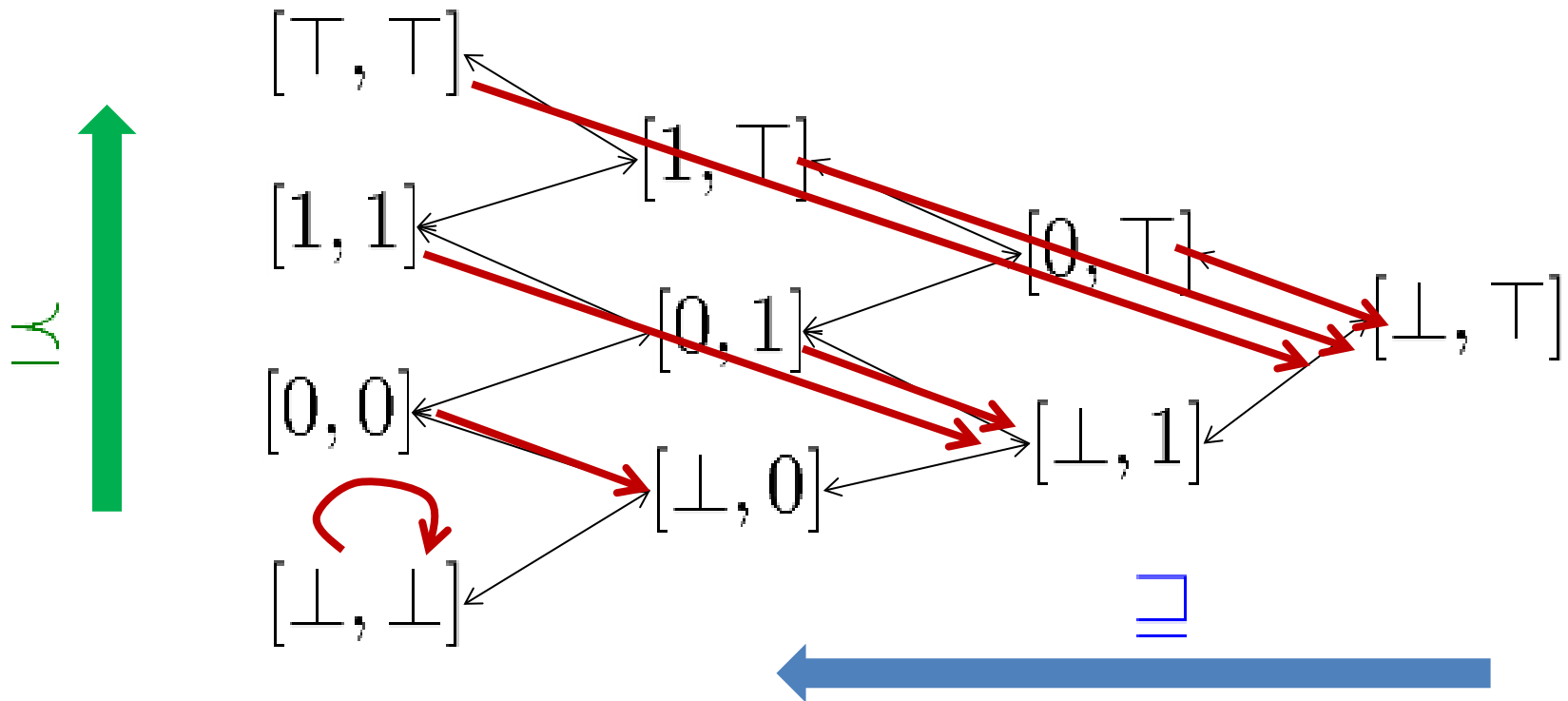


Interval Domain of Variable Statuses



Interval Domain of Variable Statuses

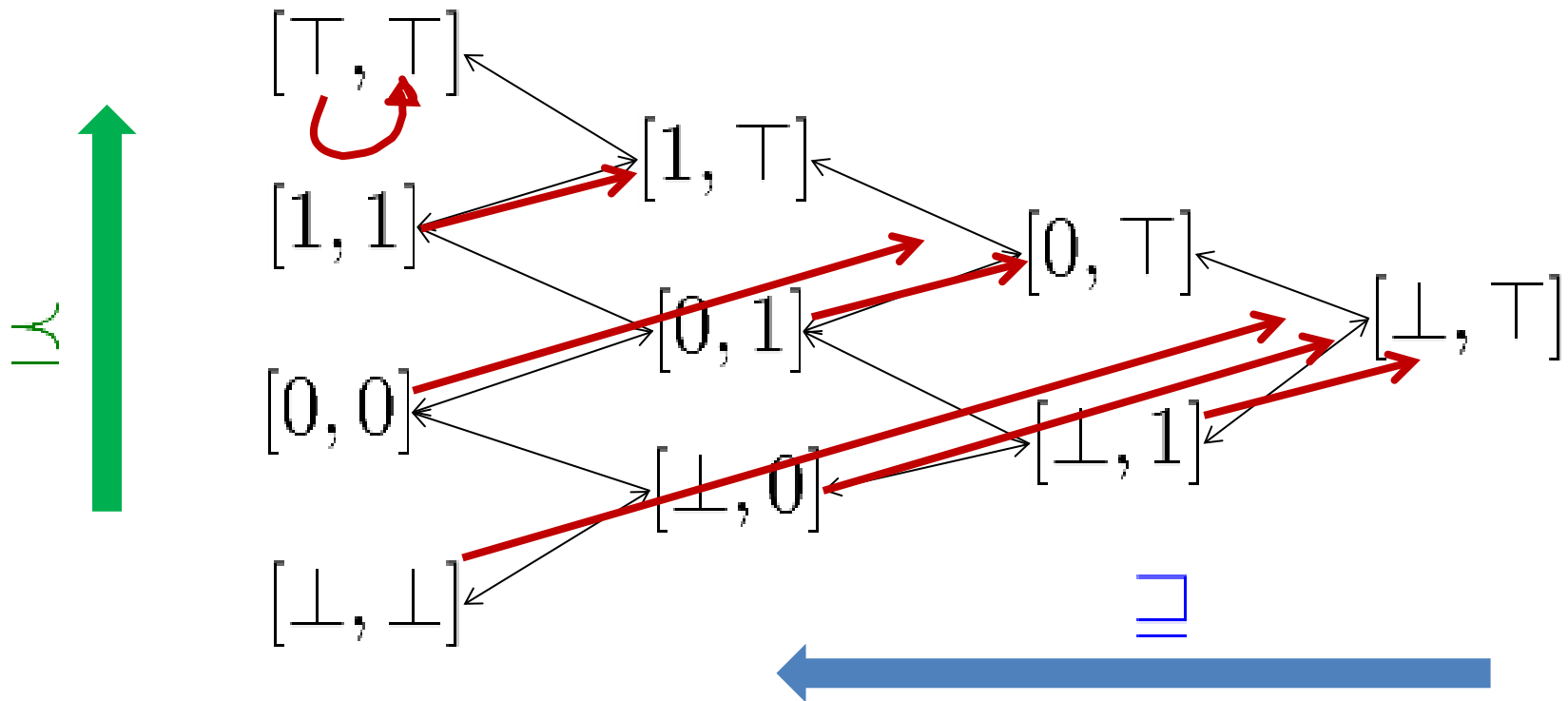
$I(\mathbb{D})$



Upper Projection *low* extracts "cannot" information

Interval Domain of Variable Statuses

$I(\mathbb{D})$



Lower Projection *low* extracts "must" information

Berry Constructiveness

Theorem 3 (Berry Semantics). For *reset-free* program P and ternary environment C ,

- $s \in \text{must}(P, C)$ iff $s^1 \in \llbracket P \rrbracket_C^0$ and
- $s \in \text{cannot}(P, C)$ iff $s^0 \in \llbracket P \rrbracket_C^0$.

Thus, Berry's must-cannot fixed point response of *reset-free* P coincides with $\mu C. \llbracket P \rrbracket_C^0$.

Definition 2. A program P is

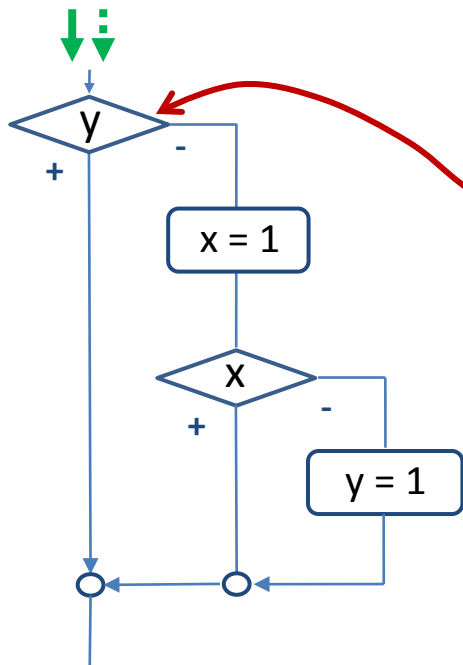
- **Berry-constructive** iff $\forall x \in \mathcal{V}. \mu C. \llbracket P \rrbracket_C^0(x) \in \{0, 1\}$;
- **strongly Berry-constructive** iff $\forall x \in \mathcal{V}. \mu C. \llbracket P \rrbracket_C^\perp(x) \in \{\perp, 0, 1\}$.

Theorem 4. P is strongly Berry-constructive $\Rightarrow P$ is Berry-constructive.

4 EXAMPLES

if y else ($x = 1$; if x else $y = 1$)

$$S_0 = \{x^0, y^0\}$$



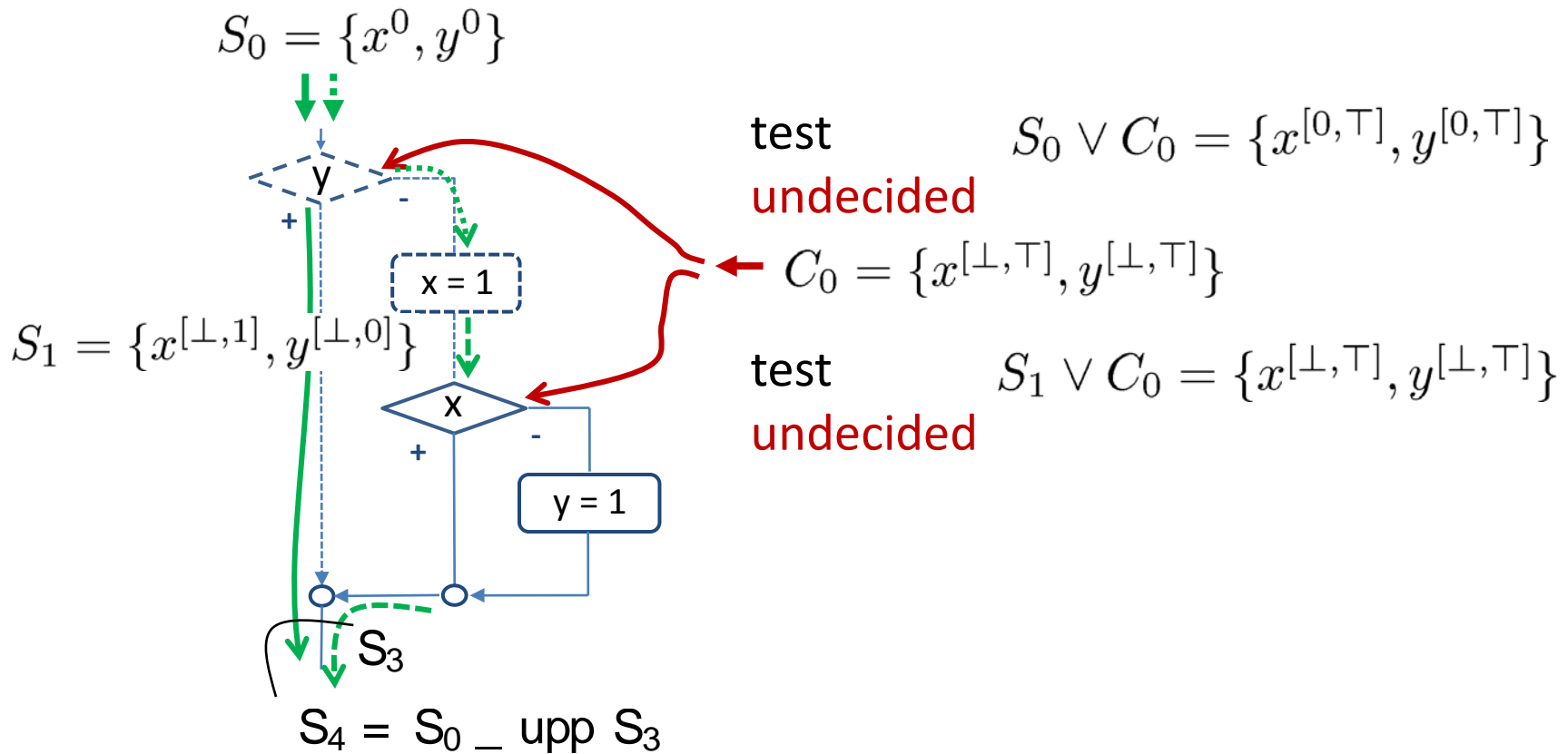
test

undecided

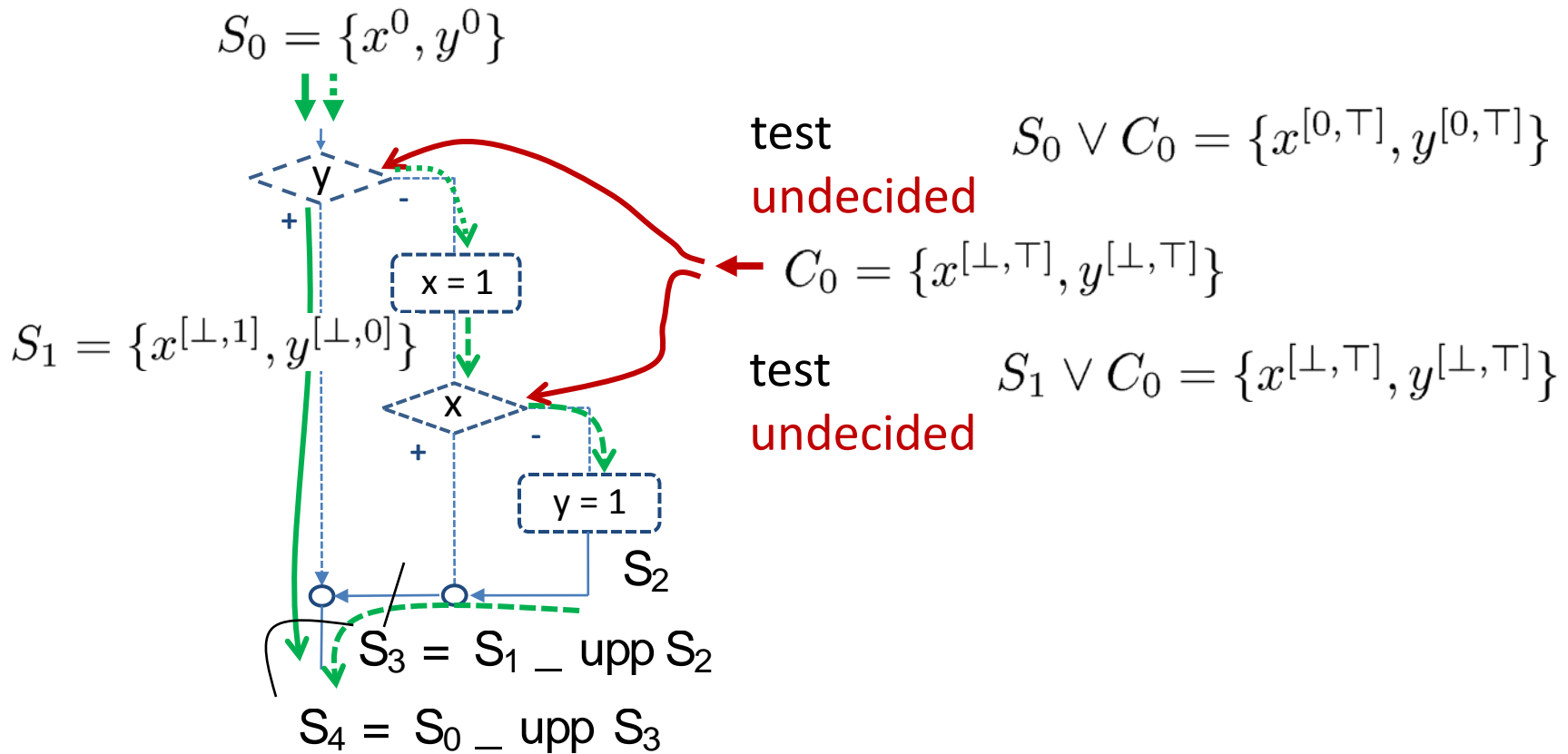
$$S_0 \vee C_0 = \{x^{[0, \top]}, y^{[0, \top]}\}$$

$$C_0 = \{x^{[\perp, \top]}, y^{[\perp, \top]}\}$$

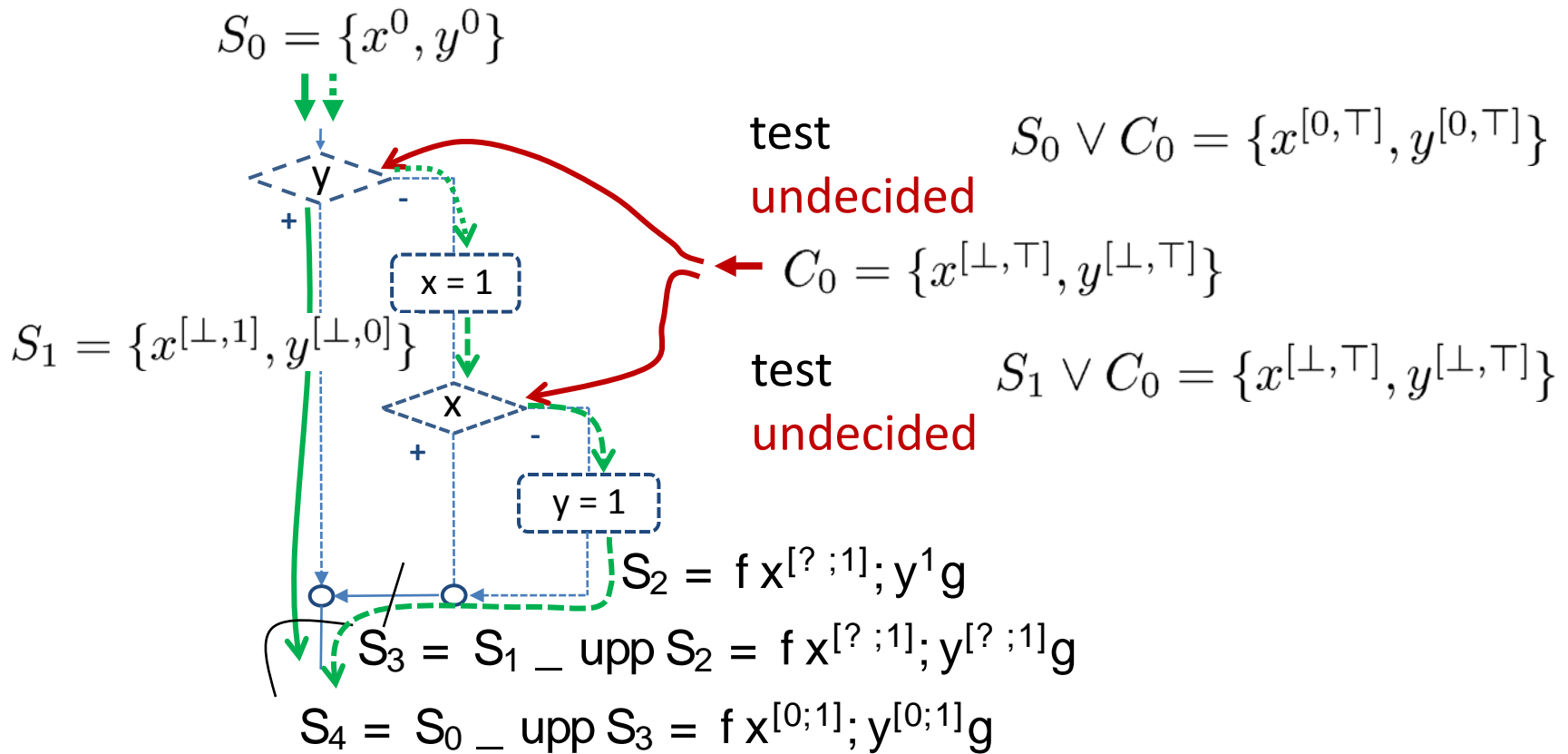
if y else ($x = 1$; if x else $y = 1$)



if y else ($x = 1$; if x else $y = 1$)

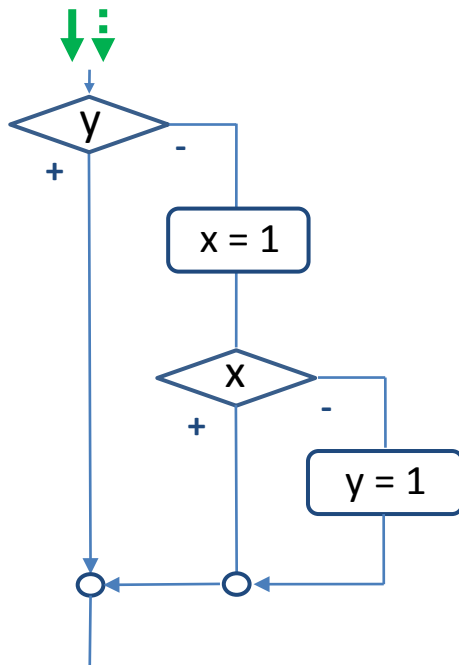


if y else ($x = 1$; if x else $y = 1$)



if y else ($x = 1$; if x else $y = 1$)

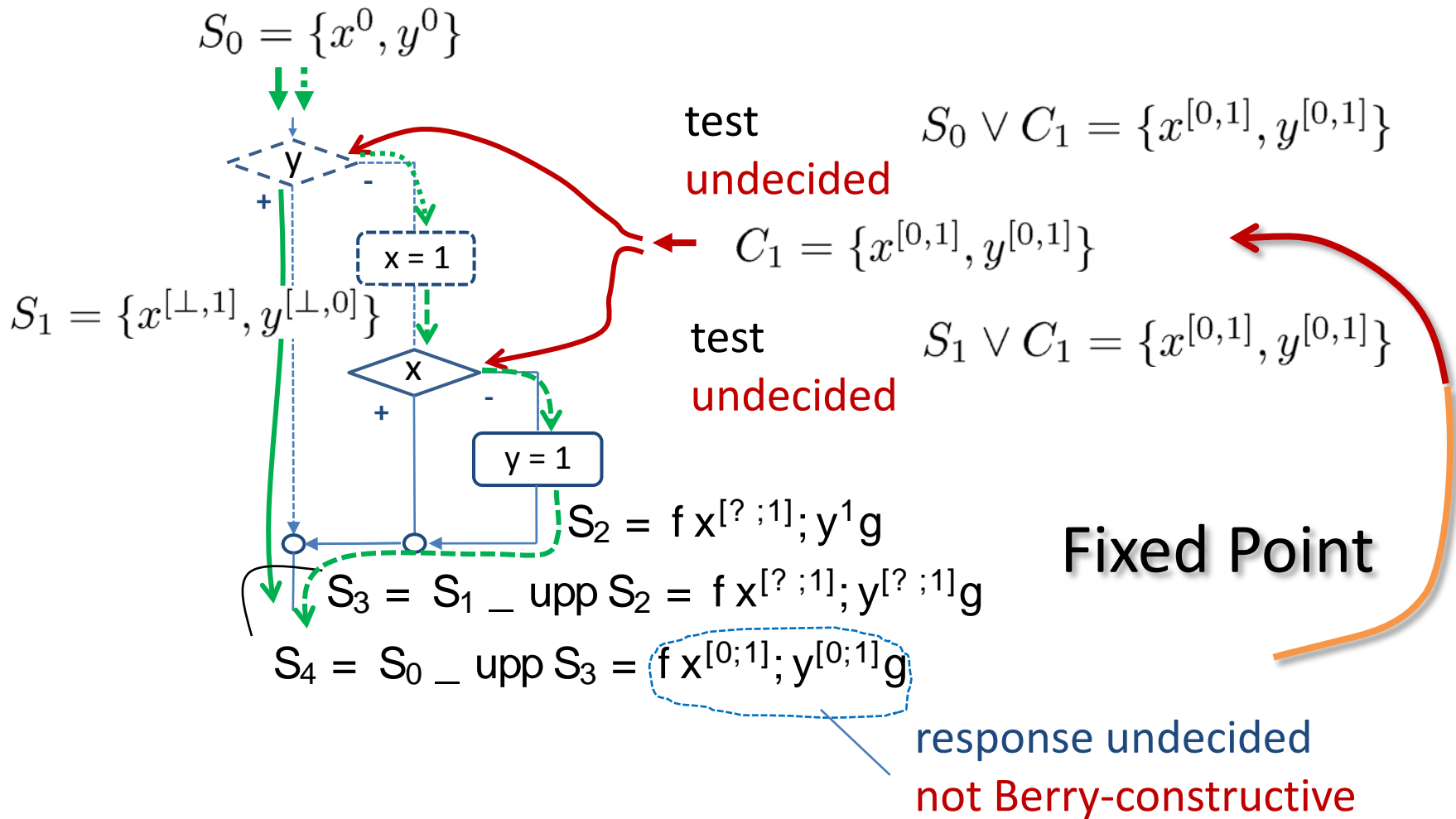
$$S_0 = \{x^0, y^0\}$$



← $C_1 = \{x^{[0,1]}, y^{[0,1]}\}$

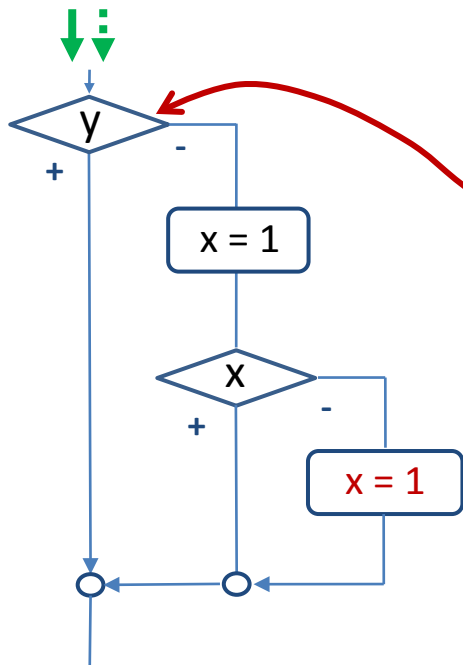
$$S_4 = S_0 \text{ _ upp } S_3 = f x^{[0;1]}; y^{[0;1]} g$$

if y else ($x = 1$; if x else $y = 1$)



if y else ($x = 1$; if x else $x = 1$)

$$S_0 = \{x^0, y^0\}$$



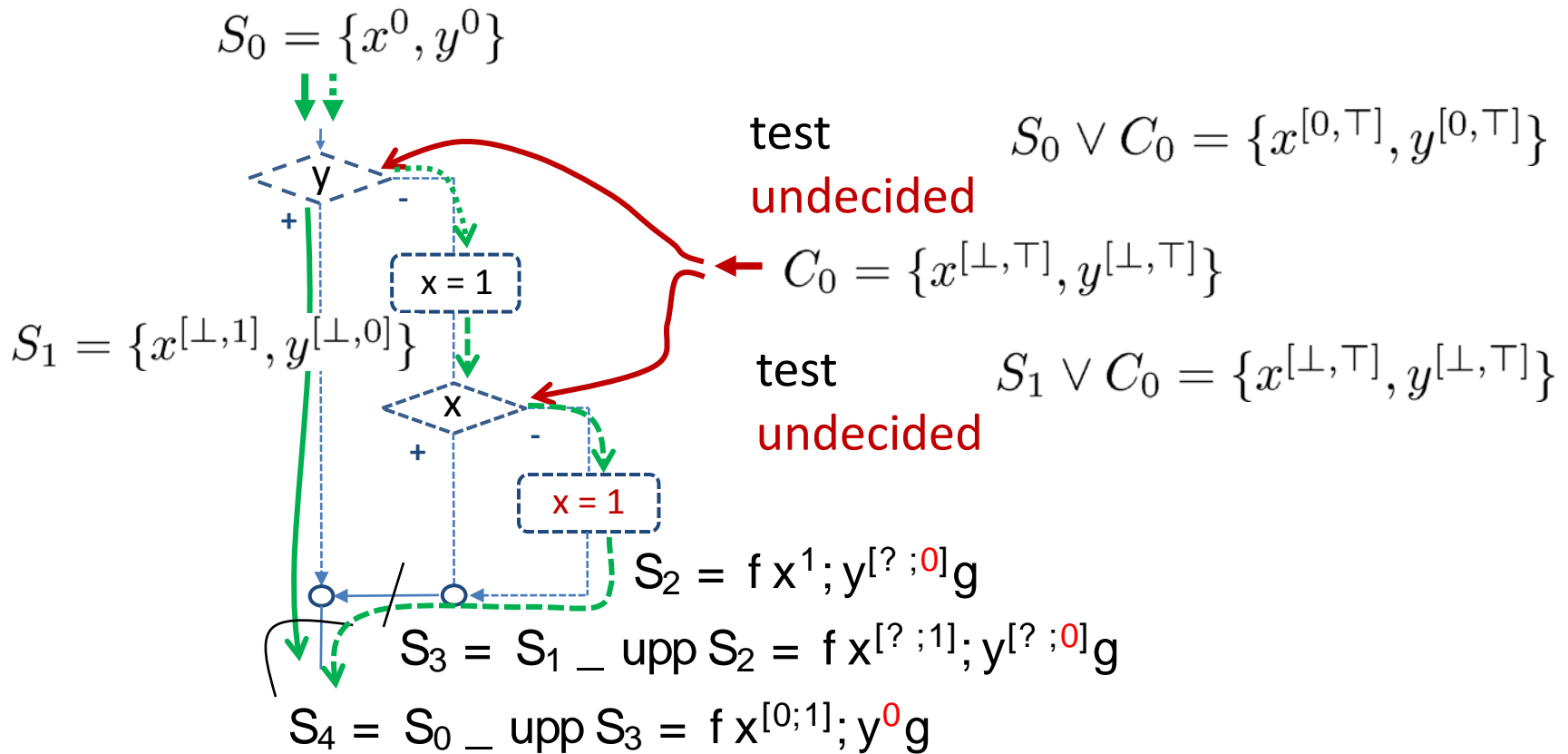
test

undecided

$$S_0 \vee C_0 = \{x^{[0, \top]}, y^{[0, \top]}\}$$

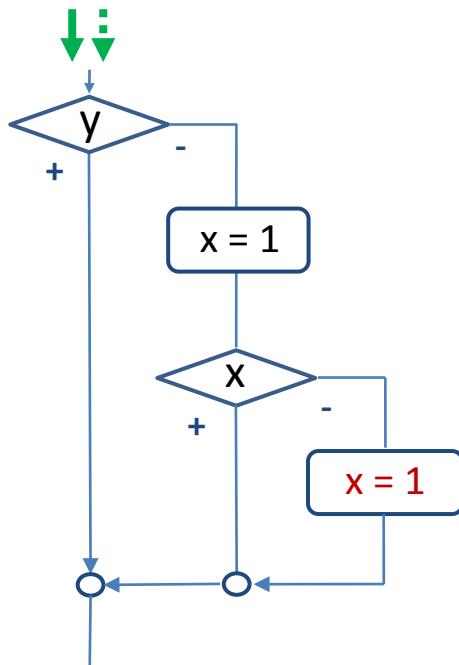
$$C_0 = \{x^{[\perp, \top]}, y^{[\perp, \top]}\}$$

if y else ($x = 1$; if x else $x = 1$)



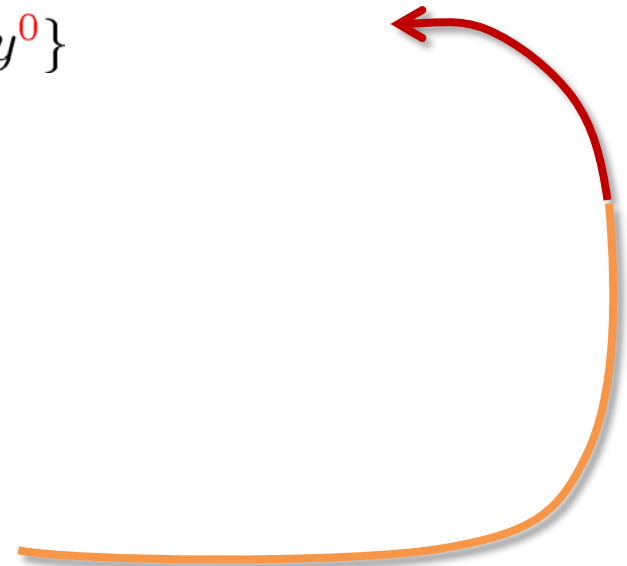
if y else ($x = 1$; if x else $x = 1$)

$$S_0 = \{x^0, y^0\}$$

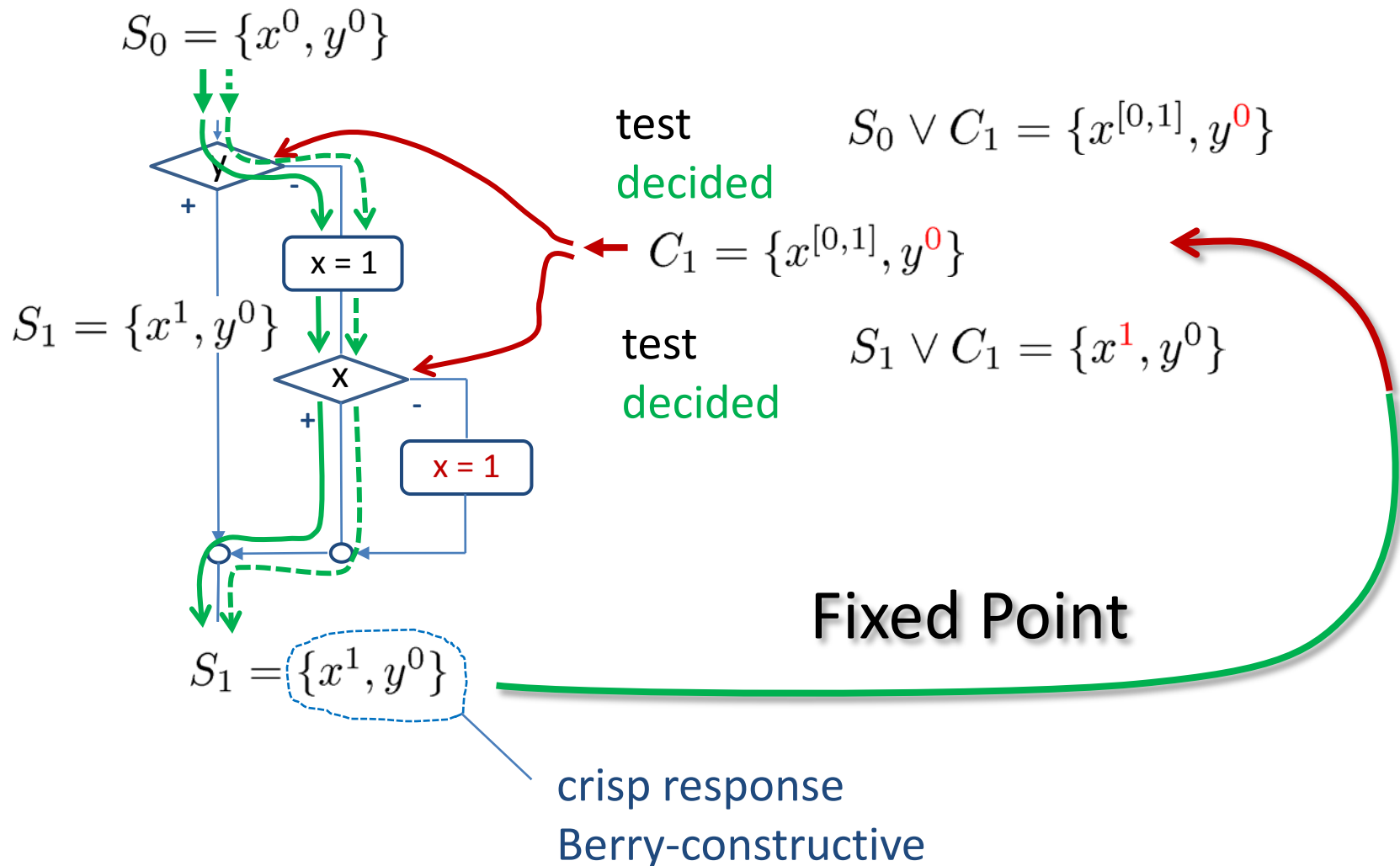


$$\leftarrow C_1 = \{x^{[0,1]}, y^0\}$$

$$S_4 = S_0 \text{ _ upp } S_3 = f x^{[0,1]}; y^0 g$$



if y else ($x = 1$; if x else $x = 1$)



5 CONCLUSION

Conclusions

- Signals can be emulated and generalised using shared variables + synchronisation constraints.
 -) „SC-Thesis“: Signals in Esterel are syntactic sugar.
- Sequential Constructiveness permits
 - arbitrary (init;update;read;)*-tick cycles.
- Berry-constructive reactions correspond to
 - a single (init;update;read;)¹-tick cycle;
 - fixed point analysis on sequential-parallel lattice $I(\mathbb{D})$
- Yes, SC is a conservative extension of BC

Open Problems

- extend results to full Esterel (V7) syntax
- develop fixed-point semantics for SC