C, Java vs. Synchronous Programming A Sequentially Constructive Program

### Synchronous Languages—Lecture 22

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Sequentially Constructive Concurrency

# Implementing (Deterministic) Concurrency

Motivatio

- C, Java, etc.:
  - 🙂 Familiar
  - © Expressive sequential paradigm
  - © Concurrent threads unpredictable in functionality and timing

#### **Synchronous Programming**:

- © Unfamiliar to most programmers
- © Restrictive in practice

**Aim:** Deterministic concurrency with synchronous foundations, but without synchronous restrictions.

Motivatio

Analy

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Slide 3

Motivation eness (SC) A Sequentially Constructive Program

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### Safety-Critical Embedded Systems

Synchronous Languages

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- Embedded systems often safety-critical
- Safety-critical systems must react deterministically

Slide 1

- Computations often exploit concurrency
- Key challenge: Concurrency must be deterministic!

Thanks to Michael Mendler (U Bamberg) for support with these slides

# Comparing Both Worlds

### Sequential Languages

- ► C, Java, ...
- Asynchronous schedule
  - o By default: Multiple concurrent readers/writers
  - On demand: Single assignment synchronization (locks, semaphores)
- Imperative
  - o All sequential control flow prescriptive
  - o Resolved by programmer

#### Synchronous Languages

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A Sequentially Constructive Program

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- Esterel, Lustre, Signal, SCADE, SyncCharts ...
- Clocked, cyclic schedule
  - o By default: Single writer per cycle, all reads initialized
  - On demand: Separate multiple assignments by clock barrier (pause, wait)
- Declarative
  - All micro-steps sequential control flow descriptive
  - o Resolved by scheduler



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# Comparing Both Worlds (Cont'd)

Sequential Languages	Synchronous Languages		Control
<ul> <li>Asynchronous schedule</li> <li>No guarantees of determinism or deadlock freedom</li> <li>Intuitive programming paradigm</li> <li>Sequentially Constructive Model of C</li> <li>Deterministic concurrency and deadloc</li> <li>Intuitive programming paradigm</li> </ul>		Request req pend	checkReq grant Dispatch grant free
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Implementing Deterministic Concurrency: SC MoC

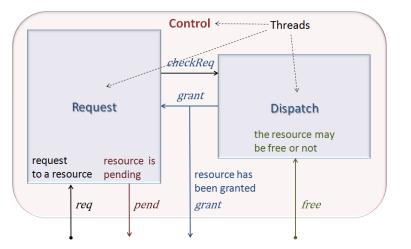
- **Concurrent** micro-step control flow:
  - © Descriptive
  - © Resolved by scheduler
  - ${\begin{tabular}{ll} \textcircled{\begin{tabular}{ll} \hline \hline \hline \\ \hline \end{array}} Deterministic concurrency and deadlock freedom \\$
- **Sequential** micro-step control flow:
  - © Prescriptive
  - © Resolved by the programmer
  - $\odot \implies {\sf Intuitive \ programming \ paradigm}$

# A Sequentially Constructive Program (Cont'd)

Motivation

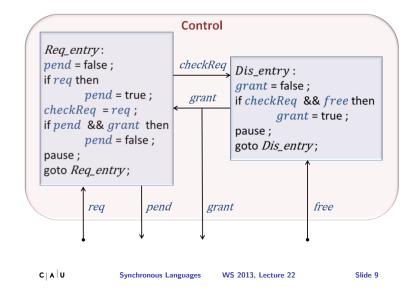
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A Sequentially Constructive Program



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# A Sequentially Constructive Program (Cont'd)

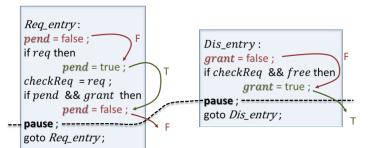


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# A Sequentially Constructive Program (Cont'd)

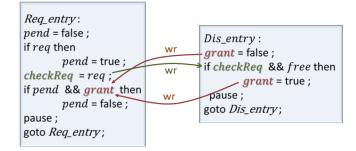
Motivation



Imperative program order (sequential access to shared variables)

- "write-after-write" can change value sequentially
- Prescribed by programmer
  - ☺ Accepted in SC MoC
  - © Not permitted in standard synchronous MoC

# A Sequentially Constructive Program (Cont'd)



**Concurrency** scheduling constraints (access to shared variables):

- "write-before-read" for concurrent write/reads
- "write-before-write" (*i. e.*, conflicts!) for concurrent & non-confluent writes
- Micro-tick thread scheduling prohibits race conditions
- Implemented by the SC compiler

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Sequentially Constructive Schedulability Concurrent Variable Accesses Sequential Admissibility

### Overview

#### Motivation

### Sequential Constructiveness (SC)

Sequentially Constructive Schedulability Concurrent Variable Accesses Sequential Admissibility

### Analyzing SC

# A Constructive Game of Schedulability

rogrammer

logically reactive program

Programmer

Defines the rules

estribes sequent

Free Schedules'

Determines winning strate

and deadlock freedom

"Admissible Schedules"

admissible schedules

Run-time = Opponent

piler = Player

rtion<sup>p</sup>rondernmer

Compiler

2

Run-time system

Slide 14

Leaves concurrency to compiler and run-time

Restricts concurrency to ensure determinacy

Tries to choose a spoiling execution from

Sequential Constructiveness (SC) **Concurrent Variable Accesses** 

### Organizing Concurrent Variable Accesses

SC Concurrent Memory Access Protocol (per macro tick)





concurrent, multi-writer, multi-reader variables

concur

Confluent Statements (per macro tick)

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For all me Mem, read in macro t

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Sequentially Constructive Schedulability Sequential Constructiveness (SC) Sequential Constructiveness (SC) **Concurrent Variable Accesses Concurrent Variable Accesses** Sequential Admissibility – Basic Idea Types of Writes I Given **two writes** to x, distinguish Confluent writes Order of the writes does not matter Sequentially ordered variable accesses Precondition: No side effects Are enforced by the programmer Non-confluent writes Cannot be reordered by compiler or run-time platform Order of the writes does matter

- - Generate potential data races
  - Must be resolved by the compiler
  - Can be ordered under multi-threading and run-time

The following applies to **concurrent** variable accesses only ...

We also generalize the notion of confluence to pairs of arbitrary statements, if their execution order does not matter.

#### Also distinguish

- Effective writes, which change value of x
- Ineffective writes, which do not change value of x

Note: Given two identical writes x = e; x = e,

- these are confluent
- the 2nd write is ineffective

Exhibit no races

#### Only concurrent writes/reads to the same variable

Sequentially Constructive Schedulability Concurrent Variable Accesses Sequential Admissibility

### Combination Functions

Combination function f:

•  $f(f(x, e_1), e_2) = f(f(x, e_2), e_1)$ 

► Examples: \*, +, -, max, and, or

for all side-effect free expressions  $e_1, e_2$ 

► Sufficient condition: f is commutative and associative

Sequentially Constructive Schedulabili Concurrent Variable Accesses Sequential Admissibility

### Scheduling Relations I

For macro tick R, and **concurrent but not confluent** node instances (executed statements)  $ni_1$ ,  $ni_2$ , define scheduling relations:

 $ni_1 \rightarrow^R ni_2$ : "happens before" (linear order)

▶  $ni_1$  occurs before  $ni_2$  in R

 $ni_1 \leftrightarrow_{ww}^R ni_2$ : "write / write conflict"

- $ni_1$  and  $ni_2$  both perform absolute writes on the same variable
- or both perform relative writes of different type on the same variable
- Impossible to find linear order!

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# Types of Writes II

**Relative writes**, of type f ("increment" / "modify"): x = f(x, e)

- *f* must be a combination function
- Evaluation of *e* must be free of side effects
- Thus, schedules 'x = f(x, e<sub>1</sub>); x = f(x, e<sub>2</sub>)' and 'x = f(x, e<sub>2</sub>); x = f(x, e<sub>1</sub>)' yield same result for x
- Thus, writes are confluent
- ► E.g., x++, x = 5 \* x, x = x-10

Absolute writes ("write" / "initialize"): x = e

- Writes that are not relative
- ► E.g., x = 0, x = 2 \* y + 5, x = f(z)

### Scheduling Relations II

For macro tick R, and **concurrent but not confluent** node instances (executed statements)  $ni_1$ ,  $ni_2$ , define scheduling relations:

- $ni_1 \rightarrow_{wr}^R ni_2$ : "write before read", or "initialize before read"
  - ▶ *ni*<sub>1</sub> is absolute write
  - $ni_2$  is read of the same variable
- $ni_1 \rightarrow_{ir}^R ni_2$ : "increment before read", or "update before read"
  - *ni*<sub>1</sub> is relative write
  - ► *ni*<sub>2</sub> is read of the same variable
- $ni_1 \rightarrow_{wi}^R ni_2$ : "write before increment", or "initialize before update"
  - *ni*<sub>1</sub> is absolute write
  - *ni*<sub>2</sub> is relative write of the same variable

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### Sequential Admissibility

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$\textit{ni}_1  ightarrow^{R} \textit{ni}_2$	:	"happens before"
$ni_1 \leftrightarrow^R_{ww} ni_2$	:	"write / write"
$ni_1 \rightarrow^R_{wr} ni_2$	:	"write before read"
$ni_1 \rightarrow^R_{ir} ni_2$	:	"increment before read"
$ni_1  ightarrow^R_{wi} ni_2$	:	"write before increment"

**Definition:** A run is SC-admissible iff for all macro ticks *R* and all node instances  $ni_1$ ,  $ni_2$  in *R*:  $\neg(ni_1 \leftrightarrow_{ww}^R ni_2) \land$  $((ni_1 \rightarrow_{wr}^R ni_2) \lor (ni_1 \rightarrow_{i_r}^R ni_2) \lor (ni_1 \rightarrow_{w_i}^R ni_2)) \Rightarrow ni_1 \rightarrow^R ni_2)$ 

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SC compiler

run-time

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Motivation Sequential Constructiveness (SC) Analyzing SC Conservative Static Approximation Acyclic Sequential Constructiveness (ASC)

### Overview

#### Motivation

Sequential Constructiveness (SC)

#### Analyzing SC

Conservative Static Approximation Acyclic Sequential Constructiveness (ASC) Conclusion

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- Use a relation  $n_1|n_2$  to over-approximate  $n_1|_R n_2$ , *i. e.*, what statements are concurrently invoked in the same tick,
  - by considering only static control flow, or
  - ignoring dependency on initial conditions, or
  - by falsely considering nodes to be in the same tick.

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- May not recognize confluence
- May not recognize that writes are relative

Synchronous Languages

**Definition:** A program is sequentially constructive (SC) iff for each initial configuration and input sequence:

programmer

- 1. There exists an SC-admissible run
- 2. Every SC-admissible run generates the same determinate sequence of macro responses

Motivation Sequential Constructiveness (SC) Analyzing SC

# Acyclic Sequential Constructiveness - Definition

 By over-approximating concurrency and confluence the static node relations

$$n_1 \leftrightarrow_{ww} n_2$$
,  $n_1 \rightarrow_{wr} n_2$ ,  $n_1 \rightarrow_{ir} n_2$ , and  $n_1 \rightarrow_{wi} n_2$ 

are computed.

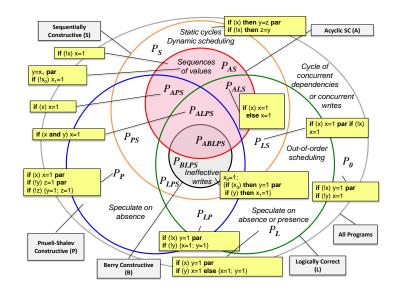
- A suitable over-approximation of →<sup>R</sup> is the (transitive closure) of the static control flow relation n<sub>1</sub> →<sub>seq</sub> n<sub>2</sub> (program order).
- $\blacktriangleright$  Let  $\rightarrow$  be defined as the following union:

$$\rightarrow := \rightarrow_{seq} \bigcup \rightarrow_{wr} \bigcup \rightarrow_{ir} \bigcup \rightarrow_{wi} \bigcup \leftrightarrow_{ww}$$

**Definition:** A program is acyclic SC (ASC) schedulable iff in its sequential-concurrent control flow graph (SCG) all  $\rightarrow$  cycles consist entirely of  $\rightarrow_{seq}$  edges.

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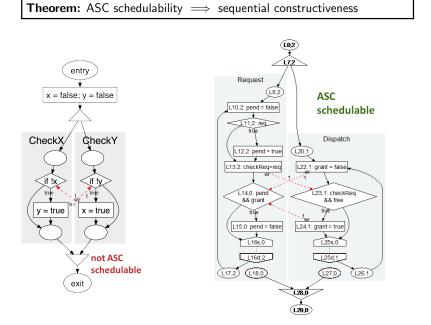
# Synchronous Program Classes



Sequential Constructiveness (SC)	Conservative Static Approximation Acyclic Sequential Constructiveness (ASC) Conclusion
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# Conclusions

- Clocked, synchronous model of execution for imperative, shared-memory multi-threading
- Conservatively extends synchronous programming (Esterel) by standard sequential control flow (Java, C)
- $\blacktriangleright \implies$  Deterministic concurrency with synchronous foundations, but without synchronous restrictions
  - $\blacktriangleright$   $\hfill \ensuremath{\textcircled{}}$  Expressive and intuitive sequential paradigm
  - © Predictable concurrent threads



Motivation Conservative Static Approximation Sequential Constructiveness (SC) Acyclic Sequential Constructiveness (ASC) Analyzing SC Conclusion

### To Go Further

- DFG-funded PRETSY Project: www.pretsy.org
- R. von Hanxleden, M. Mendler, J. Aguado, B. Duderstadt, I. Fuhrmann, C. Motika, S. Mercer, and O. O'Brien. Sequentially Constructive Concurrency – A conservative extension of the synchronous model of computation. In Proc. Design, Automation and Test in Europe Conference (DATE'13), Grenoble, France, March 2013. http://rtsys.informatik. uni-kiel.de/~biblio/downloads/papers/date13.pdf
- R. von Hanxleden, M. Mendler, J. Aguado, B. Duderstadt, I. Fuhrmann, C. Motika, S. Mercer, O. O'Brien, and Partha Roop. Sequentially Constructive Concurrency – A Conservative Extension of the Synchronous Model of Computation. Technical Report 1308, Christian-Albrechts-Universitaet zu Kiel, Department of Computer Science, Aug 2013. http://rtsys.informatik.uni-kiel.de/ ~biblio/downloads/papers/report-13seqc.pdf
- G. Berry. *The foundations of Esterel*. In G. Plotkin, C. Stirling, and M. Tofte, editors, Proof, Language, and Interaction: Essays in Honour of Robin Milner, pages 425-454, Cambridge, MA, USA, 2000.

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