

Agenda des Blockseminars

Embedded Real-Time Systems

24. Juni 2023

Zeit	Thema
9:15	<i>Begrüßung</i>
9:20	Highly-Optimizing and Multi-Target Compiler for Embedded System Models
10:05	Cache Interference-aware Task Partitioning for Real-time Multi-core Systems
10:35	A Synergistic Approach to Predictable Compilation and Scheduling on Commodity Multi-Cores
11:05	<i>Pause</i>
11:20	Optimization Techniques for Time-Critical Cyber-Physical Systems
11:50	Temporal Property-Based Testing of a Timed C Compiler using Time-Flow Graph Semantics
12:20	Symmetric Distributed Applications
12:50	<i>Mittagspause</i>
14:20	Harmonisierung von Safety, Security und Performance in Embedded Systems: Die SysML-Sec Methodik
14:50	A Rule-Based Approach for Safety Analysis Using STAMP/STPA
15:20	A Safety Analysis Method Using Fault Tree Analysis and Petri Nets
15:50	<i>Schlusswort</i>