

# Agenda des Blockseminars

## Synchrone Sprachen

2. März 2020

<b>Zeit</b>	<b>Thema</b>
08:55	<i>Begrüßung</i>
09:00	Representation of Time in Hybrid Co-Simulation
09:45	ThingML: A Language and Code Generation Framework for Heterogeneous Targets
10:15	Skitter: A DSL for Distributed Reactive Workflows
10:45	<i>Kaffeepause</i>
11:00	Worst-Case Reaction Time Optimization on Deterministic Multi-Core Architectures with Synchronous Languages
11:30	Semantics Driven Hardware Design, Implementation, and Verification with ReWire
12:00	Dynamic Wireless Network Reconfiguration for Control System applied to a Nuclear Reactor Case Study
12:30	<i>Mittagessen</i>
13:30	Hardware-Accelerated Energy-Efficient Synchronization and Communication for Ultra-Low-Power Tightly Coupled Clusters
14:00	HardScope – Preventing DOP Attacks
14:30	Sicherheitsmuster für Automobilsysteme
15:00	<i>Kaffeepause</i>
15:15	Goal-Based Modeling and Analysis of Non-Functional Requirements
15:45	Semi Automated Conversation from Data Flow Diagrams to Program Models
16:15	<i>Schlusswort</i>