

# Mixing interaction and computation on FPGA

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## Design and implementation of ECLAT

- a declarative (OCAML-like) language compiled to circuits
- for programming reactive hardware applications on FPGA
  - based on a logical time = the global clock of the circuit
  - able to simply express long-running computations
- with an extension for interacting with the FPGA environment

# A (typed) call-by-value $\lambda$ -calculus with let-polymorphism

expression  $e ::= c$   
|  $x$   
| **fun**  $p \rightarrow e$   
|  $e e$   
| **if**  $e$  **then**  $e$  **else**  $e$   
|  $(e,e)$   
| **let**  $p = e$  **in**  $e$

pattern  $p ::= x \mid (p,p)$

constant  $c ::= \text{true} \mid \text{false} \mid N \mid + \mid - \mid < \mid > \mid =$

value  $v ::= c \mid \text{fun } p \rightarrow e \mid (v,v)$

abbreviation  $(\text{let } f \text{ } p = e \text{ in } e') \equiv (\text{let } f = \text{fun } p \rightarrow e \text{ in } e')$

## Reduction semantics

**reduction relation**  $e \longrightarrow e'$

context  $E ::= \square e \mid v \square \mid \text{if } \square \text{ then } e \text{ else } e$   
 $\mid (\square, e) \mid (v, \square) \mid \text{let } p = \square \text{ in } e$

reduction rules:

$E[e] \longrightarrow E[e']$ if $e \longrightarrow e'$	(CONTEXT)
$(\text{fun } p \rightarrow e) v \longrightarrow e[p \mapsto v]$	(APP)
$\text{if true then } e_1 \text{ else } e_2 \longrightarrow e_1$	(IF-TRUE)
$\text{if false then } e_1 \text{ else } e_2 \longrightarrow e_2$	(IF-FALSE)
$\text{let } p = v \text{ in } e \longrightarrow e[p \mapsto v]$	(LET)
$c v \longrightarrow \text{call}(c, v)$	(CALL)

## Example: a combinational circuit

```
let not a = if a then false else true in
let xor (a,b) = if a then not b else b in
let or (a,b) = if a then true else b in
let land (a,b) = if a then b else false in
```

```
let half_add(a,b) = (xor(a,b), land(a,b)) in
```

```
let full_add(a,b,ci) =
  let (s1,c1) = half_add(a,b) in
  let (s,c2) = half_add(ci,s1) in
  let co = or(c1,c2) in
  (s, co)
```

```
in
```

```
full_add (* entry point, applied to input values at each clock tick *)
```

## Expressing long-running computations

- $e ::= \dots \mid \text{pause } e$
- new reduction rule:  $E[\text{pause } e] \longrightarrow \text{pause } E[e]$  (PAUSE)
- but:  $\text{pause } e \not\rightarrow$
- $e \longrightarrow^* e'$ : a suite of zero or more reductions
- A close expression  $e$  is either **instantaneous** ( $e \longrightarrow^* v$ ) or **non-instantaneous** ( $e \longrightarrow^* \text{pause } e'$ )
- Behavioral semantics:  $e \Longrightarrow e'$  (reduction in one clock cycle)

$$\frac{\text{TICK-VAL} \quad e \longrightarrow^* v}{e \Longrightarrow v}$$

$$\frac{\text{TICK-PAUSE} \quad e \longrightarrow^* \text{pause } e'}{e \Longrightarrow e'}$$

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## Example

`if (pause true) then (let x = true in (pause x)) else false`

$\longrightarrow$  `pause (if true then (let x = true in (pause x)) else false)`

$\implies$  `(if true then (let x = true in (pause x)) else false)`

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## Parallel composition (returns a pair of values)

$e ::= \dots \mid (e \parallel e)$

$E ::= \dots \mid (\square \parallel e) \mid (v \parallel \square) \mid (\text{pause } e \parallel \square)$

$(v \parallel v') \longrightarrow (v, v') \quad (\text{PAR-VAL})$

$E[\text{pause } e] \longrightarrow \text{pause } E[e] \quad \text{if } E \neq (\square \parallel \text{pause } e') \quad (\text{PAUSE})$

$(\text{pause } e \parallel \text{pause } e') \longrightarrow \text{pause } (e \parallel e') \quad (\text{PAR-PAUSE})$



# Recursive functions

expression  $e ::= \dots \mid \text{fix } f (\text{fun } p \rightarrow e)$

value  $v ::= \dots \mid \text{fix } f (\text{fun } p \rightarrow e)$

- $\underbrace{(\text{fix } f (\text{fun } p \rightarrow e))}_{\phi} v \longrightarrow \text{pause } (e[f \mapsto \phi] v) \quad (\text{FIX})$

Derivated construct:  $(\text{let rec } f \text{ } p = e \text{ in } e')$

$\equiv \text{let } f = \text{fix } f (\text{fun } p \rightarrow e) \text{ in } e'$

Limitation: the compiler supports only tail-recursion.

## Example: a long-running computation

```
let rec gcd(a,b) = (* does not always terminate, e.g., gcd(1,-1) *)
  if a < b then gcd(a,b-a)
  else if a > b then gcd(a-b,b)
  else a in
let x = gcd(2,2) in
let (x1,x2) = (gcd(18,12) || gcd(5,10)) in
let s = x1 + x2 in
(x,s) (* see on slide 15 how to run such a computation
in reactive programs *)
```

clock ticks	$t_0$	$t_1$	$t_2$	$t_3$	$t_4$
x	$\varepsilon$	2	2	2	2
x1		$\varepsilon$	$\varepsilon$	$\varepsilon$	6
x2		$\varepsilon$	$\varepsilon$	5	5
s					11

## Stateful computation (for interaction)

$e ::= \dots \mid \text{reg}_\ell e \text{ last } e \quad (\text{e.g., } \text{reg}_\ell (\text{fun } c \rightarrow c + 1) \text{ last } 0)$

This construct instantaneously returns a value.

It uses a local memory for saving values between instantaneous execution of the program.

Labels ( $\ell$ ) are omitted in source programs.

Reduction relation extended with states  $\mu: e/\mu \longrightarrow e'/\mu'$

Non-standard substitution:  $e[x \mapsto v]$  means “replace each occurrence of  $x$  in  $e$  by an instance of value  $v$ ”.

Instantiation renames labels in the same way at each execution.

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$e ::= \dots \mid \text{reg}_\ell e \text{ last } e \quad (\text{e.g., } \text{reg}_\ell (\text{fun } c \rightarrow c + 1) \text{ last } 0)$

$E ::= \dots \mid \text{reg}_\ell \square \text{ last } e \mid \text{reg}_\ell v \text{ last } \square$

$$\overbrace{\text{reg}_\ell w \text{ last } v}^e / \mu \longrightarrow e / \mu [\ell \mapsto w v] \quad \text{if } \ell \notin \text{dom}(\mu) \quad (\text{REG-INIT})$$
$$\text{reg}_\ell w \text{ last } v / \mu \longrightarrow v' / \mu [\ell \mapsto w v'] \quad \text{if } \mu(\ell) = v' \quad (\text{REG-NEXT})$$

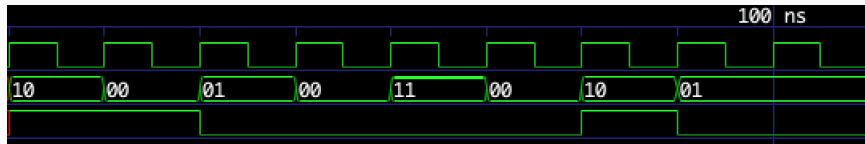
REG-CONTEXT

$$\frac{\mu(\ell) = e \quad e / \mu \longrightarrow e' / \mu'}{\text{reg}_\ell w \text{ last } v / \mu \longrightarrow \text{reg}_\ell w \text{ last } v / \mu' [\ell \mapsto e']}$$

## Example: await

```
let await ( i , reset ) =  
  let step s = (s or i) & not reset in  
  reg step last false  
in  
await  (* entry point, applied to input values at each clock tick *)
```

Simulation of the VHDL generated code:



## Example: ABCRO

```
let fby (a,b) =  
  let step (pre,cur) = (cur,b) in  
  let (cur,_) = reg step last (a,a) in cur in
```

```
let edge i = not (fby( false ,i)) & i in
```

```
let abro ((a,b),r) = edge (await (a,r) & await(b,r)) in
```

```
let abcro (((a,b),c),r) = abro ((abro((a,b),r),c),r)  
in abcro (* entry point, applied to input values at each clock tick *)
```



## Mix interaction and computation

Accept registers with non-instantaneous update functions:

`regℓ w last`  $v/\mu \longrightarrow v/\mu[\ell \mapsto e]$  if  $\mu(\ell) = \text{pause } e$  (DEFAULT)

derivated construct:

`exec`  $e_1$  `default`  $e_2 \equiv \text{reg} (\text{fun } \_ \rightarrow (e_1, \text{true})) \text{ last } (e_2, \text{false})$

## Example

```
let main (((a,b), r ),(( x,y), suspend)) =  
  if suspend then 0  
  else (let (v,rdy) = exec gcd(x,y) default 0 in  
        if abcro(((a,b),rdy), r) then v  
        else 42)  
in  
main (* entry point, applied to input values at each clock tick *)
```

- 
- when suspend, returns 0 without activating the *else* branch
  - otherwise,
    - run step-by-step the computation gcd(x,y)
    - use the *rdy* output of construct `exec` as input *c* for `abcro`



## Type-based reactivity analysis (1/3)

**type**  $\tau ::= \text{bool} \mid \text{int} \mid \tau \times \tau \mid \tau \xrightarrow{\delta} \tau \mid \alpha$

**type scheme**  $\sigma ::= \forall \alpha. \tau$

**response time**  $\delta ::= N \mid \max(\delta, \delta) \mid \delta + \delta$

Judgement  $\boxed{\Gamma \vdash e : \tau \mid \delta}$ , means “in the typing environment  $\Gamma$ , expression  $e$  has type  $\tau$  and worst-case response time  $\delta$ ”.

A close program  $e$  is reactive if  $\emptyset \vdash e : \tau \mid \mathbf{0}$ .

## Type-based reactivity analysis (2/3)

TY-CONST

$$\frac{\Delta(c) = \sigma}{\Gamma \vdash c : \text{instance}(\sigma, \Gamma) | \mathbf{0}}$$

TY-VAR

$$\frac{\Gamma(x) = \sigma}{\Gamma \vdash x : \text{instance}(\sigma, \Gamma) | \mathbf{0}}$$

TY-PAUSE

$$\frac{\Gamma \vdash e : \tau | \delta}{\Gamma \vdash \text{pause } e : \tau | \mathbf{1} + \delta}$$

TY-LET

$$\frac{\Gamma \vdash e : \tau | \delta \quad \Gamma[p \mapsto \text{gen}(\tau, \Gamma)] \vdash e' : \tau' | \delta'}{\Gamma \vdash \text{let } p = e \text{ in } e' : \tau' | \delta + \delta'}$$

TY-IF

$$\frac{\Gamma \vdash e : \text{bool} | \delta \quad \Gamma \vdash e_i : \tau | \delta_i \quad i \in \{1, 2\}}{\Gamma \vdash \text{if } e \text{ then } e_1 \text{ else } e_2 : \tau | \delta + \max(\delta_1, \delta_2)}$$

TY-SUB

$$\frac{\Gamma \vdash e : \tau | \delta}{\Gamma \vdash e : \tau | \delta + \mathbf{N}}$$

## Type-based reactivity analysis (3/3)

TY-PAIR

$$\frac{\Gamma \vdash e_i : \tau_i | \delta_i \quad i \in \{1, 2\}}{\Gamma \vdash (e_1, e_2) : \tau_1 \times \tau_2 | \delta_1 + \delta_2}$$

TY-PAR

$$\frac{\Gamma \vdash e_i : \tau_i | \delta_i \quad i \in \{1, 2\}}{\Gamma \vdash (e_2 \parallel e_1) : \tau_1 \times \tau_2 | \max(\delta_1, \delta_2)}$$

TY-FUN

$$\frac{\Gamma[p : \tau] \vdash e' : \tau' | \delta}{\Gamma \vdash (\text{fun } p \rightarrow e) : \tau \xrightarrow{\delta} \tau' | \mathbf{0}}$$

TY-APP

$$\frac{\Gamma \vdash e_1 : \tau \xrightarrow{\delta'} \tau' | \mathbf{0} \quad \Gamma \vdash e_2 : \tau | \delta}{\Gamma \vdash e_1 e_2 : \tau' | \delta + \delta'}$$

TY-FIX

$$\frac{\Gamma[f : \tau \xrightarrow{\delta+1} \tau'][p : \tau] \vdash e : \tau' | \delta}{\Gamma \vdash \text{fix } f (\text{fun } p \rightarrow e) : \tau \xrightarrow{\delta+1} \tau' | \mathbf{0}}$$

TY-REG

$$\frac{\Gamma \vdash e : \tau \xrightarrow{\delta} \tau | \mathbf{0} \quad \Gamma \vdash e_0 : \tau | \mathbf{0}}{\Gamma \vdash \text{reg}_l e \text{ last } e_0 : \tau | \mathbf{0}}$$

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TY-REG

$$\frac{\Gamma \vdash e : \tau \xrightarrow{\delta} \tau | \mathbf{0} \quad \Gamma \vdash e_0 : \tau | \mathbf{0}}{\Gamma \vdash \text{reg}_\ell e \text{ last } e_0 : \tau | \mathbf{0}}$$

## Conclusion

A core language (ECLAT) for programming reactive hardware applications involving long-running computations [*Sylvestre, Chailloux, Sérot – WIP: mixing computation and interaction on FPGA, EMSOFT 23*]

- global clock = logical time (no need to compute a WCET)
- features sized integers, simulation primitives and global arrays implemented using RAM memory blocks

with an attempt to trade time and space:

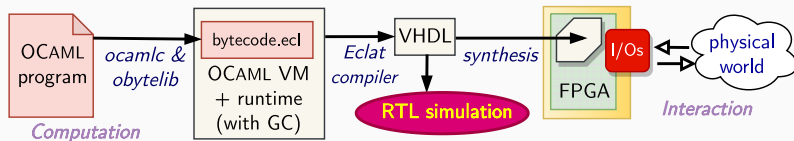
- by making function environments explicit (by  $\lambda$ -lifting),
- then, inlining non-recursive functions  
(= *augmenting the size, diminishing the throughput*)
- and sharing tail-recursive functions calls  
(= *pausing for one tick, diminishing the size*)

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A realistic application: the OCAML VM and runtime in ECLAT



[Sylvestre, Sérot, Chailloux – Hardware implementation of OCAML using a synchronous functional language, PADL 24]