SyncCharts in C

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EMSOFT'09, Grenoble, 13 October 2009



Institut für Informatik Christian-Albrechts-Universität zu Kiel

SyncCharts in C

A Proposal for Light-Weight, Deterministic Concurrency

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Light-Weight, Deterministic Concurrency in C

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Light-Weight, Deterministic Concurrency in C

If you insist, you can use it for SyncCharts ...

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Problem Statement Motivating Example: Producer-Consumer-Observer

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Problem Statement

Given: C compiler (preferrably gcc)

Problem Statement Motivating Example: Producer-Consumer-Observer

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Problem Statement

Given:

C compiler (preferrably gcc) + programmer that knows C



Problem Statement Motivating Example: Producer-Consumer-Observer

Problem Statement

Given:

C compiler (preferrably gcc) + programmer that knows C

What we want: Deterministic concurrency



Problem Statement

Given:

C compiler (preferrably gcc) + programmer that knows C

What we want:

Deterministic concurrency ... and maybe preemption, deadlock avoidance, signal handling, instantaneous communication, dynamic priorities, proper handling of schizophrenia, etc.

Problem Statement

Given:

C compiler (preferrably gcc) + programmer that knows C

What we want:

Deterministic concurrency ... and maybe preemption, deadlock avoidance, signal handling, instantaneous communication, dynamic priorities, proper handling of schizophrenia, etc.

What we don't want:

Heavy tools, special compilers, licenses, training courses, OS overhead, custom hardware, platform dependence, adaptation effort, ...

Problem Statement

Want to embed deterministic, concurrent semantics in traditional, sequential language (C)

Embedding means:

- Just add language constructs that can be expressed in C
- Do not restrict/change/extend semantics of C language
- Do not require separate preprocessors, analysis tools or compilers

Problem Statement Motivating Example: Producer-Consumer-Observer

Overview

Introduction

Problem Statement Motivating Example: Producer-Consumer-Observer

Concurrency in SC

Further SC Concepts

Wrap-Up

Motivating Example: Producer-Consumer-Observer

Precision Timed Architecture (PRET):

- Uses physical time to synchronize (DEAD instruction)
- Current programming model requires analysis of code + execution platform

Producer	Consumer	Observer
int main() {	int main() {	int main() {
DEAD (28) ;	DEAD (41) ;	DEAD(41);
volatile unsigned int * buf =	volatile unsigned int * buf =	volatile unsigned int * buf =
(unsigned int*) (0x3F800200);	(unsigned int*) (0x3F800200);	(unsigned int*) (0x3F800200);
unsigned int i = 0;	unsigned int i = 0;	volatile unsigned int * fd =
for (i = 0; ; i++) {	int arr[8];	(unsigned int*) (0x80000600);
DEAD (26) ;	for (i =0; i<8; i++)	unsigned int i = 0;
*buf = i;	arr[i] = 0;	for (i = 0; ; i++) {
}	for (i = 0; ; i++) {	DEAD (26) ;
return 0;	DEAD (26) ;	<pre>*fd = *buf;</pre>
}	register int tmp = *buf;	}
	arr[i%8] = tmp;	return 0;
	}	}
	return 0:	

Lickly et al., CASES'08

Producer-Consumer-Observer Example Dynamic Coroutines SC Thread Operators

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Overview

Introduction

Concurrency in SC

Producer-Consumer-Observer Example Dynamic Coroutines SC Thread Operators

Further SC Concepts

Wrap-Up



```
Consumer
int main() {
    DEAD(41);
    volatile unsigned int * buf =
        (unsigned int')(0x3F800200);
    unsigned int i = 0;
    int arr[8];
    for (i = 0; is(s; i++)
        arr[i] = 0;
    for (i = 0; i(s; i++) {
        DEAD(26);
        register int tmp = *buf;
        arr[i%8] = tmp;
    }
    return 0;
}
```

```
Observer
int main() {
    DEAD(41);
    volatile unsigned int * buf =
        (unsigned int*)(0x37800200);
    volatile unsigned int * fd =
        (unsigned int *)(0x80000600);
    unsigned int i = 0;
    for (i = 0; ; i++) {
    DEAD(26);
    *fd = *buf;
    }
    return 0;
}
```



```
Consumer
int main() {
DEAD(41);
volatile unsigned int * buf =
   (unsigned int *)(0x3F800200);
unsigned int i = 0;
int arr[8];
for (i = 0; ic8; i++)
   arr[i] = 0;
for (i = 0; ic8; i++) {
   DEAD(26);
   register int tmp = *buf;
   arr[i%8] = tmp;
   return 0;
}
```

```
Observer
int main() {
    DEAD(41);
    volatile unsigned int * buf =
        (unsigned int*)(0x3F800200);
    volatile unsigned int * fd =
        (unsigned int *)(0x80000600);
    unsigned int i = 0;
    for (i = 0; ; i++) {
        DEAD(26);
        *fd = *buf;
        return 0;
}
```

```
#include "sc.h"
 1
 2
 3
     // == MAIN FUNCTION ==
 4
     int main()
 5
 6
       int notDone. init = 1:
 7
 8
       do {
9
         notDone = tick(init);
10
         //sleep(1);
11
          init = 0:
12
       } while (notDone):
13
       return 0:
14
```



Consumer int main() { DEAD(41); volatile unsigned int * buf = (unsigned int*) (0x3F800200); unsigned int i = 0;int arr[8]; for (i =0; i<8; i++) arr[i] = 0;for (i = 0; ; i++) { DEAD (26); register int tmp = *buf; arr[i%8] = tmp; return 0;

```
Observer
int main() {
    DEAD(41);
    volatile unsigned int * buf =
        (unsigned int*)(0x3F800200);
    volatile unsigned int * fd =
        (unsigned int*)(0x80000600);
    unsigned int i = 0;
    for (i = 0; ; i++) {
    DEAD(26);
        *fd = *buf;
    }
    return 0;
}
```

```
#include "sc.h"
 1
 2
 3
     // == MAIN FUNCTION ==
 4
      int main()
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 6
       int notDone. init = 1:
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 8
       do {
 9
         notDone = tick(init);
10
         //sleep(1):
11
          init = 0:
12
       } while (notDone):
13
       return 0:
14
```

16	<pre>// == TICK FUNCTION ==</pre>
17	<pre>int tick(int isInit)</pre>
18	{
19	static int BUF, fd, i, j,
20	k = 0, tmp, arr [8];
21	
22	TICKSTART(isInit, 1);
23	
24	PCO:
25	FORK(Producer, 3);
26	FORK(Consumer, 2);
27	FORKE(Observer);
28	
29	Producer:
30	for $(i = 0; ; i++)$ {
31	PAUSE;
32	BUF = i; }
	-

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Consumer int main() { DEAD(41); volatile unsigned int * buf = (unsigned int*) (0x3F800200); unsigned int i = 0;int arr[8]; for (i =0; i<8; i++) arr[i] = 0;for (i = 0; ; i++) { DEAD(26); register int tmp = *buf; arr[i%8] = tmp; return 0;

```
Observer
int main() {
    DEAD(41);
    volatile unsigned int * buf =
        (unsigned int*)(0x3F800200);
    volatile unsigned int * fd =
        (unsigned int*)(0x80000600);
    unsigned int i = 0;
    for (i = 0; ; i++) {
    DEAD(26);
        *fd = *buf;
    }
    return 0;
}
```

```
#include "sc.h"
// == MAIN FUNCTION ==
int main()
{
    int notDone, init = 1;
    do {
        notDone = tick(init);
        //sleep(1);
        init = 0;
    } while (notDone);
    return 0;
}
```

16	<pre>// == TICK FUNCTION ==</pre>
17	<pre>int tick(int isInit)</pre>
18	{
19	static int BUF, fd, i, j,
20	k = 0, tmp, arr [8];
21	
22	TICKSTART(isInit, 1);
23	
24	PCO:
25	FORK(Producer, 3);
26	FORK(Consumer, 2);
27	FORKE(Observer);
28	
29	Producer:
30	for $(i = 0; ; i++)$ {
31	PAUSE;
32	BUF = i; }
	-

$\label{eq:consumer:} \begin{array}{l} \mbox{for } (j=0; \ j<8; \ j++) \\ \mbox{arr}[j]=0; \\ \mbox{for } (j=0; \ j++) \ \{ \\ \begin{array}{l} \mbox{PAUSE}; \\ \mbox{tmp}=BUF; \\ \mbox{arr}[j\ \%\ 8]=\ tmp; \ \} \end{array} \end{array}$
Observer: for (;;) { PAUSE; fd = BUF; k++;}
TICKEND; }

Producer-Consumer-Observer Example Dynamic Coroutines SC Thread Operators

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Dynamic Coroutines

Approach: Manage threads at application level Problem: C does not provide access to program counter

Producer-Consumer-Observer Example Dynamic Coroutines SC Thread Operators

Dynamic Coroutines

Approach: Manage threads at application level

Problem: C does not provide access to program counter

Solution: Program labels + computed goto

- All possible continuation points of a thread get an ordinary C label
- For each thread, maintain a coarse program counter that points to continuation label

Producer-Consumer-Observer Example Dynamic Coroutines SC Thread Operators

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SC Thread Operators

TICKSTART*(*init*, p)Start (initial) tick, assign main thread priority p.TICKENDReturn true (1) iff there is still an enabled thread.

-

SC Thread Operators

TICKSTART*(<i>init</i> , <i>p</i>)	Start (initial) tick, assign main thread priority p .
TICKEND	Return true (1) iff there is still an enabled thread.
PAUSE*+	Deactivate current thread for this tick.
TERM*	Terminate current thread.
ABORT	Abort descendant threads.
TRANS(I)	Shorthand for ABORT; GOTO(1).
SUSPEND*(cond)	Suspend (pause) thread $+$ descendants if <i>cond</i> holds.

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SC Thread Operators

TICKSTART*(<i>init</i> , <i>p</i>)	Start (initial) tick, assign main thread priority <i>p</i> .
TICKEND	Return true (1) iff there is still an enabled thread.
PAUSE*+	Deactivate current thread for this tick.
TERM*	Terminate current thread.
ABORT	Abort descendant threads.
TRANS(/)	Shorthand for ABORT; GOTO(1).
SUSPEND*(cond)	Suspend (pause) thread $+$ descendants if <i>cond</i> holds.
FORK(I, p)	Create a thread with start address I and priority p .
FORKE*(<i>I</i>)	Finalize FORK, resume at <i>I</i> .
$JOINELSE^{*+}(I_{else})$	If descendant threads have terminated normally, proceed;
	else pause, jump to <i>I_{else}</i> .
JOIN*+	Waits for descendant threads to terminated normally.
	Shorthand for I_{else} : JOINE(I_{else}).
$PRIO^{*+}(p)$	Set current thread priority to <i>p</i> .

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SC Thread Operators

TICKSTART*(<i>init</i> , <i>p</i>)	Start (initial) tick, assign main thread priority p .
TICKEND	Return true (1) iff there is still an enabled thread.
PAUSE*+	Deactivate current thread for this tick.
TERM*	Terminate current thread.
ABORT	Abort descendant threads.
TRANS(/)	Shorthand for ABORT; GOTO(1).
SUSPEND*(cond)	Suspend (pause) thread $+$ descendants if <i>cond</i> holds.
FORK(I, p)	Create a thread with start address I and priority p .
FORKE*(/)	Finalize FORK, resume at <i>I</i> .
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	else pause, jump to I_{else} .
JOIN*+	Waits for descendant threads to terminated normally.
	Shorthand for I_{else} : JOINE(I_{else}).
PRIO ^{*+} (p)	Set current thread priority to <i>p</i> .

possible thread dispatcher call

⁺ automatically generates continuation label

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Preemptions Thread Synchronization and Signals

Overview

Introduction

Concurrency in SC

Further SC Concepts

Preemptions Thread Synchronization and Signals

Wrap-Up

Recall: Producer-Consumer-Observer in SC

// == MAIN FUNCTION ==		
int main()	29	Producer:
{	30	for $(i = 0;$
int notDone, init $= 1$;	31	PAUSE;
	32	BUF = i; }
do {	33	
<pre>notDone = tick(init);</pre>	34	Consumer:
//sleep(1);	35	for $(j = 0;$
init $= 0;$	36	arr[j] = C
<pre>} while (notDone);</pre>	37	for $(j = 0;$
return 0;	38	PAUSE;
}	39	tmp = BU
	40	arr [j % 8]
// == TICK FUNCTION ==	41	
<pre>int tick(int islnit)</pre>	42	Observer:
{	43	for (;;)
static int BUF, fd, i, j,	44	PAUSE;
k = 0, tmp, arr [8];	45	fd = BUF;
	46	k++; }
TICKSTART(isInit, 1);	47	
	48	TICKEND;
PCO:	49	}
FORK(Producer, 3);		

; i++) { j < 8; j++) ٦. ; j++) { F: $= tmp; \}$

```
{
```

FORK(Consumer, 2);

FORKE(Observer);

#include "sc.h"

Recall: Producer-Consumer-Observer in SC

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```
// == MAIN FUNCTION ==
int main()
  int notDone. init = 1:
 do {
   notDone = tick(init);
   //sleep(1):
    init = 0:
  } while (notDone):
  return 0:
// == TICK FUNCTION ==
int tick(int islnit)
  static int BUF, fd, i, j,
   k = 0, tmp, arr [8]:
 TICKSTART(isInit, 1):
 PCO:
 FORK(Producer, 3):
 FORK(Consumer, 2):
```

FORKE(Observer);

#include "sc.h"

Producer: for (i = 0; ; i++) { PAUSE; BUF = i; } Consumer:

for (j = 0; j < 8; j++)arr [j] = 0;for $(j = 0; ; j++) \{$ PAUSE; tmp = BUF; arr $[j \ \% \ 8] = tmp; \}$

```
Observer:
for ( ; ; ) {
    PAUSE;
    fd = BUF;
    k++; }
```

TICKEND;



Producer-Consumer-Observer + Preemptions

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// == MAIN FUNCTION == int main() int notDone. init = 1: do { notDone = tick(init); //sleep(1): init = 0: } while (notDone): return 0: // == TICK FUNCTION == int tick (int islnit) static int BUF. fd. i. i. k = 0, tmp, arr [8]: TICKSTART(isInit, 1): PCO. FORK(Producer, 3): FORK(Consumer, 2); FORKE(Observer):

#include "sc.h"

```
Producer:
for (i = 0; : i++) {
  PAUSE:
  BUF = i: 
Consumer:
for (i = 0; j < 8; j++)
  arr[i] = 0:
for (i = 0; : i++) {
  PAUSE:
  tmp = BUF:
  arr[i \% 8] = tmp; }
Observer:
for (;;) {
  PAUSE:
  fd = BUF:
  k++: \}
TICKEND:
```



Producer-Consumer-Observer + Preemptions

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#include "sc.h" // == MAIN FUNCTION == int main() int notDone, init = 1;do { notDone = tick(init);//sleep(1); init = 0; } while (notDone); return 0; // == TICK FUNCTION == int tick (int islnit) static int BUF. fd. i. i. k = 0, tmp, arr [8]: TICKSTART(isInit, 1): PCO. FORK(Producer, 4): FORK(Consumer, 2); FORK(Observer, 3): FORKE(Parent):

Producer: for (i = 0; ; i++) { PAUSE: $BUF = i; \}$ Consumer: for (i = 0; i < 8; i++)arr[i] = 0;for (j = 0; ; j++) { PAUSE: tmp = BUF;arr[j % 8] = tmp; } Observer: for (;;) { PAUSE: fd = BUF: k++; } Parent: while (1) { PAUSE: if (k = 20)TRANS(Done): if (BUF == 10)TRANS(PCO): 3 Done[.] TERM: TICKEND:





Edwards et al., JES'07; Prochnow et al., LCTES'06





```
TICKSTART(isInit, 1);
1
 2
       FORK(T1, 6);
3
       FORK(T2, 5);
 4
       FORK(T3, 3);
       FORKE(TMain);
 5
6
7
      T1: if (PRESENT(A)) {
8
        EMIT(B);
9
        PRIO(4);
10
         if (PRESENT(C))
11
          EMIT(D);
12
        PRIO(2);
         if (PRESENT(E)) {
13
14
          EMIT(T_-);
15
          TERM; }
16
       }
17
      PAUSE:
18
      EMIT(B);
19
      TERM:
20
21
      T2: if (PRESENT(B))
22
        EMIT(C);
23
       TERM:
24
25
      T3: if (PRESENT(D))
26
        EMIT(E);
27
       TERM:
28
29
      TMain: if (PRESENT(T_)) {
30
        ABORT:
        TERM; }
31
32
       JOINELSE(TMain);
33
       TICKEND:
```

Sample Execution

```
1
       TICKSTART(isInit, 1):
 2
       FORK(T1. 6):
3
       FORK(T2, 5):
 4
       FORK(T3, 3):
 5
       FORKE(TMain):
6
7
      T1: if (PRESENT(A)) {
8
        EMIT(B):
9
        PRIO(4);
         if (PRESENT(C))
10
          EMIT(D):
11
12
        PRIO(2):
13
         if (PRESENT(E)) {
          EMIT(T_-):
14
          TERM: }
15
16
       }
17
       PAUSE:
18
       EMIT(B):
19
       TERM:
20
21
      T2: if (PRESENT(B))
22
        EMIT(C);
23
       TERM:
24
25
      T3: if (PRESENT(D))
26
        EMIT(E);
27
       TERM:
28
      TMain: if (PRESENT(T_)) {
29
30
        ABORT:
31
        TERM; }
32
       JOINELSE(TMain);
33
       TICKEND:
```

Sample Execution

```
1
     ==== TICK 0 STARTS, inputs = 01, enabled = 00
 2
     ==== Inputs (id/name): 0/A
 3
     ==== Enabled (id/state): <init>
 4
     FORK: 1/<init> forks 6/T1, active = 0103
 5
     FORK: 1/<init> forks 5/T2, active = 0143
 6
     FORK
             1/\langle \text{init} \rangle forks 3/T3, active = 0153
 7
     FORKE: 1/<init>
8
     PRESENT: 6/T1 determines A/0 as present
9
     EMIT: 6/T1 emits B/1
10
     PRIO:
              6/T1 set to priority 4
11
     PRESENT: 5/T2 determines B/1 as present
12
     EMIT: 5/T2 emits C/2
13
     TERM:
              5/T2 terminates, enabled = 073
14
     PRESENT: 4/_L73 determines C/2 as present
15
     EMIT:
              4/_L73 emits D/3
16
     PRIO:
              4/_L73 set to priority 2
17
     PRESENT: 3/T3 determines D/3 as present
18
     EMIT:
              3/T3 emits E/4
19
     TERM:
              3/T3 terminates, enabled = 017
20
     PRESENT: 2/_L76 determines E/4 as present
21
     EMIT: 2/_L76 emits T_/5
22
     TERM: 2/_L76 terminates, enabled = 07
23
     PRESENT: 1/TMain determines T_/5 as present
24
     ABORT: 1/TMain disables 070, enabled = 03
25
     TERM: 1/TMain terminates, enabled = 03
26
     ==== TICK 0 terminates after 22 instructions
27
     ==== Enabled (id/state): 0/_L_TICKEND
28
     ==== Resulting signals (name/id): 0/A, 1/B, 2/C, 3/D, 4/E, 5/T_,
            Outputs OK.
```

Introduction Assessment Concurrency in SC Summary Further SC Concepts Where This Might be Going

Overview

Introduction

- Concurrency in SC
- Further SC Concepts

Wrap-Up Assessment Summary Where This Might be Going

Assessment Summary Where This Might be Going

A D b 4 A b

How Light-Weight is It?

Program perspective

Thread management

- Context: PC (1 word), descs (1 thread bit vector), parent (1 thread id), active and enabled (2 bits in thread bitvector)
- Context switch: 1 BSR instruction (on x86) + array lookup

How Light-Weight is It?

Program perspective

Thread management

- Context: PC (1 word), descs (1 thread bit vector), parent (1 thread id), active and enabled (2 bits in thread bitvector)
- ► Context switch: 1 BSR instruction (on x86) + array lookup

Source code

Very dense operator encoding

How Light-Weight is It?

Program perspective

Thread management

- Context: PC (1 word), descs (1 thread bit vector), parent (1 thread id), active and enabled (2 bits in thread bitvector)
- ► Context switch: 1 BSR instruction (on x86) + array lookup

Source code

Very dense operator encoding

Executable

Most operators translate to handful of assembler statements

How Light-Weight is It?

Programmer perspective

Installation effort

- Free download and documentation: http://www.informatik.uni-kiel.de/rtsys/sc/
- Total tar ball is \approx 50 K, mostly examples
- Really need just sc.h (and, on non-x86, selectCid.c)
- No further tools or Makefile adaptations

How Light-Weight is It?

Programmer perspective

Installation effort

- Free download and documentation: http://www.informatik.uni-kiel.de/rtsys/sc/
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- No further tools or Makefile adaptations

Mental effort

- Operators needed to get started fit on one slide
- ▶ For exact understanding, can look at sc.h (< 1 Kloc)

Assessment Summary Where This Might be Going

Limitations

SC Programming model

- Shared address space
- Continuation labels must be in tick function, not nested in functions
- Instantaneous communication patterns require manual priority assignment

Assessment Summary Where This Might be Going

Limitations

SC Programming model

- Shared address space
- Continuation labels must be in tick function, not nested in functions
- Instantaneous communication patterns require manual priority assignment

Current implementation

- Uses computed goto (gcc)
- Thread sets represented as bit vectors (unsigned int)
- No. of threads limited by word size

Assessment Summary Where This Might be Going

Summary

SyncCharts in C

- Light-weight approach to embed deterministic reactive control flow constructs into widely used programming language
- Fairly small number of primitives suffices to cover all of SyncCharts
- Multi-threaded, priority-based approach inspired by synchronous reactive processing—where it required special hw + special compiler

Assessment Summary Where This Might be Going

Not Covered Today

In the paper (proceedings, frozen at SC 1.3.3)

- Reactive processing heritage
- Experimental results
- Related work (lots of it)

Assessment Summary Where This Might be Going

Not Covered Today

In the paper (proceedings, frozen at SC 1.3.3)

- Reactive processing heritage
- Experimental results
- Related work (lots of it)

In the full report (on-line, at SC 1.5)

- Valued signals, PRE, suspension, schizophrenia handling
- Realization
- Many further examples

Where This Might be Going

SC can be used ...

- ... as programming language
- ... as intermediate target language for synthesizing graphical SyncChart models into tracable executable code
- ▶ ... as language for programming PRET/reactive architectures
- ... as a virtual machine instruction set

Where This Might be Going

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- ... as programming language
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- ... as a virtual machine instruction set

Further future work

Get people to try it out (some already did—thanks!)

Where This Might be Going

SC can be used ...

- ... as programming language
- ... as intermediate target language for synthesizing graphical SyncChart models into tracable executable code
- ▶ ... as language for programming PRET/reactive architectures
- ... as a virtual machine instruction set

Further future work

- Get people to try it out (some already did—thanks!)
- Assistance with priority assignment
- Adapt to C++, Java
- Consider multi core

Where This Might be Going

SC can be used ...

- ... as programming language
- ... as intermediate target language for synthesizing graphical SyncChart models into tracable executable code
- ▶ ... as language for programming PRET/reactive architectures
- ... as a virtual machine instruction set

Further future work

- Get people to try it out (some already did—thanks!)
- Assistance with priority assignment
- Adapt to C++, Java
- Consider multi core

Questions/Comments?

Appendix

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Overview

Background Explaining the (Original) Title Inspiration: Reactive Processing

Other SC Operators

The SC Signal Operators Further Operators

Experimental Results

Related Work

Explaining the (Original) Title Inspiration: Reactive Processing

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Explaining the (Original) Title: SyncCharts ...

Reactive control flow:

- Sequentiality
- + Concurrency
- + Preemption

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Statecharts [Harel 1987]:

- Reactive control flow
- + Visual syntax

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Explaining the (Original) Title: SyncCharts ...

Reactive control flow:

- Sequentiality
- + Concurrency
- + Preemption

Statecharts [Harel 1987]:

- Reactive control flow
- + Visual syntax

SyncCharts [André 1996]:

- Statecharts concept
- Synchronous semantics

Explaining the (Original) Title Inspiration: Reactive Processing

... in C

Today's Scenario 1: Develop model in SyncCharts, synthesize C

© Can use visual syntax



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Explaining the (Original) Title Inspiration: Reactive Processing

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- © Can use visual syntax
- Seed special modeling tool
- © Cannot directly use full power of classical imperative language

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SyncCharts in C scenario: Use SC Operators

- $\hfill {\ensuremath{\mathbb C}}$ Light weight to implement and to execute
- © Just need regular compiler
- © Semantics grounded in synchronous model

Explaining the (Original) Title Inspiration: Reactive Processing

The inspiration: Reactive processing





Li et al., ASPLOS'06

- SC multi-threading very close to Kiel Esterel Processor
- Difference: KEP dispatches at every instrClk, SC only at specific SC operators (such as PAUSE, PRIO)

The SC Signal Operators Further Operators

The SC Signal Operators

SIGNAL(S)	Initialize a local signal S.
EMIT(S)	Emit signal S.
$PRESENT(S, I_{else})$	If S is present, proceed normally; else, jump to I_{else} .
EMITINT(S, val)	Emit valued signal S , of type integer, with value val .
EMITINTMUL(<i>S</i> , <i>val</i>)	Emit valued signal S , of type integer, combined with multiplication, with value <i>val</i> .
VAL(S, reg)	Retrieve value of signal S , into register/variable <i>reg</i> .
PRESENTPRE(<i>S</i> , <i>l_{else}</i>)	If S was present in previous tick, proceed normally; else, jump to I_{else} . If S is a signal local to thread t, consider last preceeding tick in which t was active, <i>i. e.</i> , not suspended.
VALPRE(<i>S</i> , <i>reg</i>)	Retrieve value of signal S at previous tick, into register/variable reg .

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The SC Signal Operators Further Operators

Further Operators

GOTO(/)	Jump to label <i>I</i> .
CALL(<i>I</i> , <i>I</i> _{ret})	Call function <i>I</i> (eg, an on exit function), return
	to I _{ret} .
RET	Return from function call.
ISAT(<i>id</i> , <i>I</i> _{state} , <i>I</i>)	If thread <i>id</i> is at state l_{state} , then proceed to next instruction (<i>e.g.</i> , an on exit function associated with <i>id</i> at state l_{state}). Else, jump to label <i>l</i> .
PPAUSE [*] (p, l)	Shorthand for $PRIO(p, I')$; I' : $PAUSE(I)$ (saves one call to dispatcher).
JPPAUSE [*] (p, I _{then} , I _{else})	Shorthand for JOIN(<i>I</i> _{then} , <i>I</i>); <i>I</i> : PPAUSE(<i>p</i> , <i>I</i> _{else}) (saves another call to dispatcher).
ISATCALL(<i>id</i> , <i>I</i> _{state} , <i>I</i> _{action} , <i>I</i>)	Shorthand for ISAT(<i>id</i> , I_{state} , I); CALL(I_{action} , I)

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Conciseness



Size of tick function in C source code, line count without empty lines and comments

Code Size



Size of tick function object code, in Kbytes

Code Size



Size of executable, in Kbytes

Performance



Accumulated run times of tick function, in thousands of clock cycles

Operator Density



SC operations count, ratio to clock cycles

Related Work (Lots Of It ...)

- Synchronous language extensions: Reactive C [Boussinot '91], ECL [Lavagno & Sentovich '99], FairThreads [Boussinot '06], Lusteral [Mendler & Pouzet '08]
- Compilation of synchronous programs [Berry, Edwards, Potop-Butucaru, ...]
- BAL virtual machine [Edwards & Zeng '07]
- PRET [Edwards, Lee et al.'08], PRET-C [Roop et al.'09], SHIM [Tardieu & Edwards '06]
- Numerous Statechart dialects (Statemate, Stateflow, SCADE, ASCET, UML, ...)
- Statecharts and FMSs in C/C++ [Samek '08, Wagner et al.'06]
- Compilation of Statecharts [Ali & Tanaka '00, Wasowski '03], SyncCharts [André '03]
- Compilation for reactive processors [Li et al.'06, Yuan et al.'08]